

• Victor Jorgensen



TECHNICAL MANUAL

DFC 402 8/1-70  
MADEFORDELING

ANTAL PLADER = 66  
ANTAL KABELSTIK = 8  
ANTAL RESERVEREDE POSITIONER = 0

TYPE	ANTAL	RC-NR.	KREDSLØB
1	7	834-1	12AC401
2	5	839-1	7AC402
3	2	838-1	4AC403
5	1	860-2	4DD402
14	1	884-1	9BA403/1AA405
16	4	835-1	6BC401
17	3	847-1	7AG401
23	1	8016-38	38 POL ELCO
24	7	8016-90	90 POL ELCO
26	1	837-1	3BB401
35	1	836-1	5AC404
37	1	890-1	11DB402/1DB403
41	1	894-1	4DB404/1DB405
43	1	858-1	1AG402
50	1	893-2	1FF402
55	3	898-1	1DB406
56	6	897-1	10DC405
58	1	901-1	1AJ408
60	1	903-1/	1AJ407/1BG407/1CC406
69	1	909-1/	6CB402
72	2	933-1	8DB409
73	8	934-1	3BF403
74	8	935-1	3BD404
75	2	936-1	18DC408
76	1	834-2	12AM401
77	4	3032-1	12AD401

DFC 402 8/1-70  
 DOUBLE POSITION LIST

FORMAT = 3 x 26

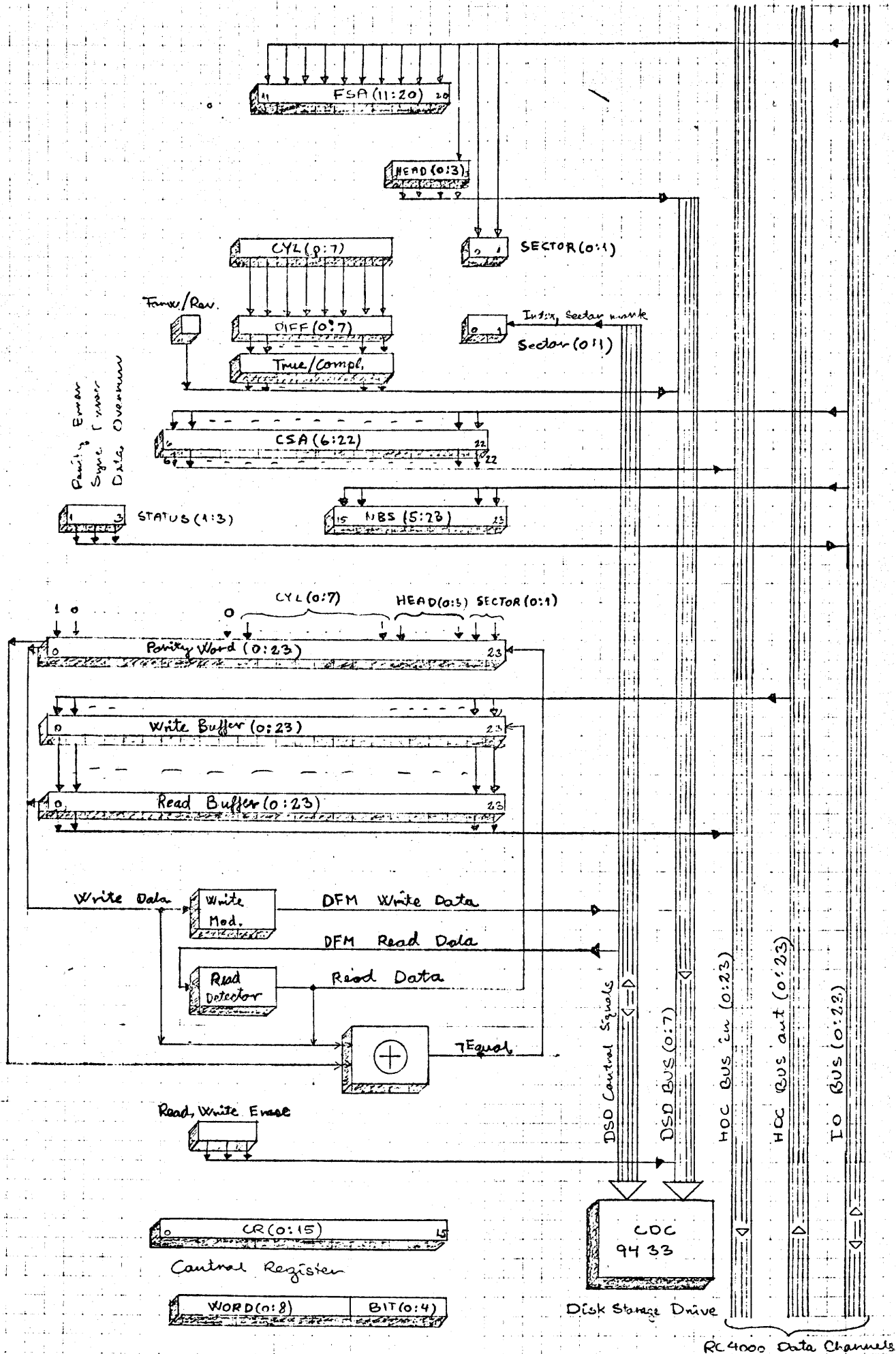
IND	AKT	TYP	SYM	TYP
1	64	16	49	72
2	66	16	50	72
3	62	60	70	56
4	65	69	52	75
5	67	1	53	75
6	21	2	36	37
7	44	77	23	17
8	10	14	22	1
9	9	17	9	17
10	16	74	8	14
11	15	74	57	35
12	45	1	18	77
13	75	50	19	74
14	74	5	17	74
15	23	3	11	74
16	22	1	10	74
17	14	74	31	73
18	12	77	30	73
19	13	74	21	76
20	41	74	51	43
21	19	76	6	2
22	8	1	16	1
23	7	17	15	3
24	42	74	46	17
25	40	74	44	73
26	39	74	45	73
27	70	16	76	55
28	50	77	77	55
29	68	26	78	55
30	18	73	35	73
31	17	73	34	73
32	33	73	33	73
33	32	73	32	73
34	31	73	71	56
35	30	73	72	56
36	6	37	73	56
37	46	2	74	56
38	48	1	55	58
39	47	77	26	74
40	69	2	25	74
41	72	16	20	74
42	49	2	24	74
43	51	1	56	2
44	25	73	7	77
45	26	73	12	1
46	24	17	37	2
47	71	3	39	77
48	52	1	38	1
49	1	72	42	2
50	2	72	28	77
51	20	43	43	1
52	4	75	48	1
53	5	75		
54	73	41		

55	38	58		
56	43	2		
57	11	35		
62			3	60
63			75	56
64			1	16
65			4	69
66			2	16
67			5	1
68			29	26
69			40	2
70	3	56	27	16
71	34	56	47	3
72	35	56	41	16
73	36	56	54	41
74	37	56	14	5
75	63	56	13	50
76	27	55		
77	28	55		
78	29	55		

KABP	X	Y	TYPE
1360	37	393	24
1361	37	-30	24
1362	195	395	24
1363	195	-30	24
1364	350	395	24
1365	350	-30	24
1366	-60	55	24
1369	-60	105	23

Unit	RC 4000
Dwg. No.	

# OFC 402 DATA CHANNEL STRUCTURE



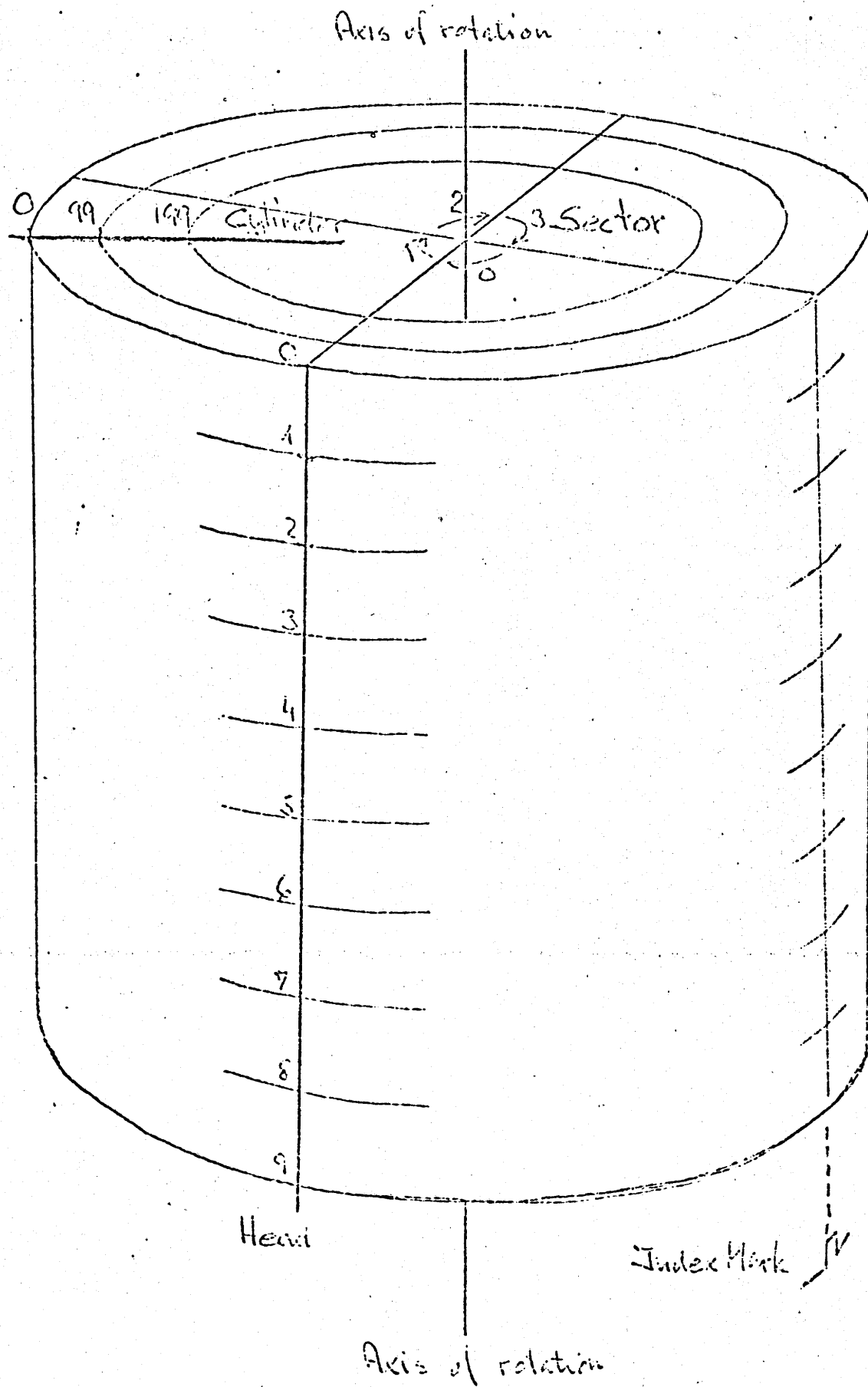
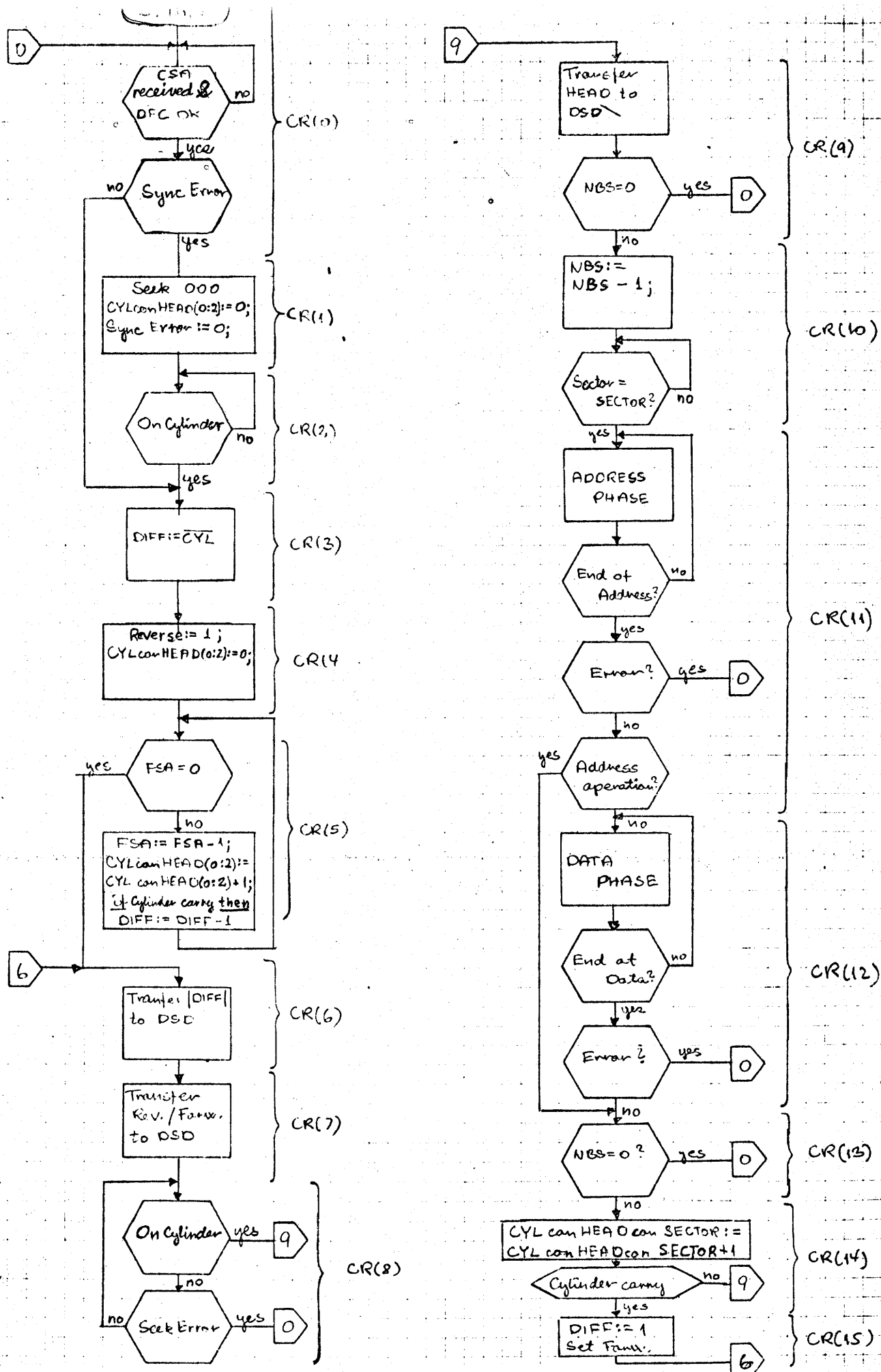


Figure .

Location of Cylinders, Heads, and Sectors on a Disc Kit.

The Disc Segment Address is  $40 \times \text{Cylinder} + 4 \times \text{Head} + \text{Sector}$

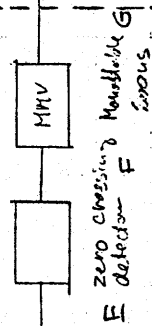
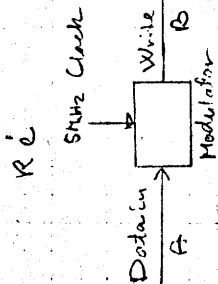


DFC 402  
FLOW DIAGRAM

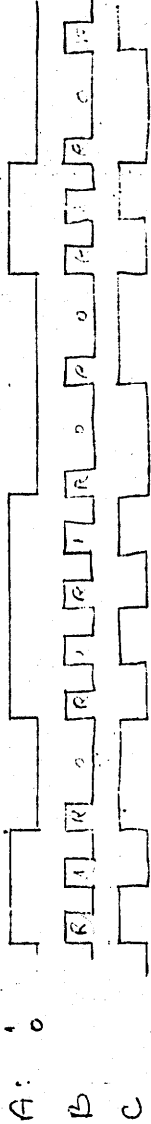
Unit

Dwg. No.

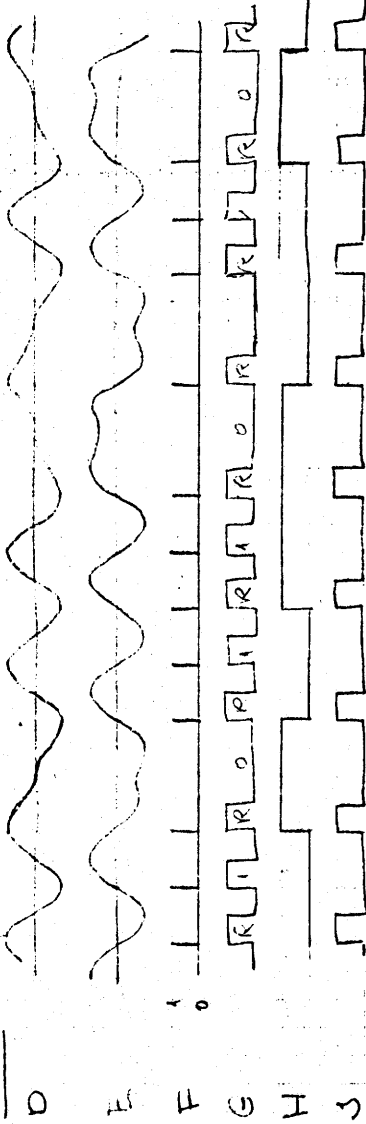
# base-circuits CDC clock unit.



Write:



Read:



Unit: DFC	Designed 7.10.69	Read - Write
Approved	Checked	Signature
Checked	Sheet	Sheet
Last Revision		





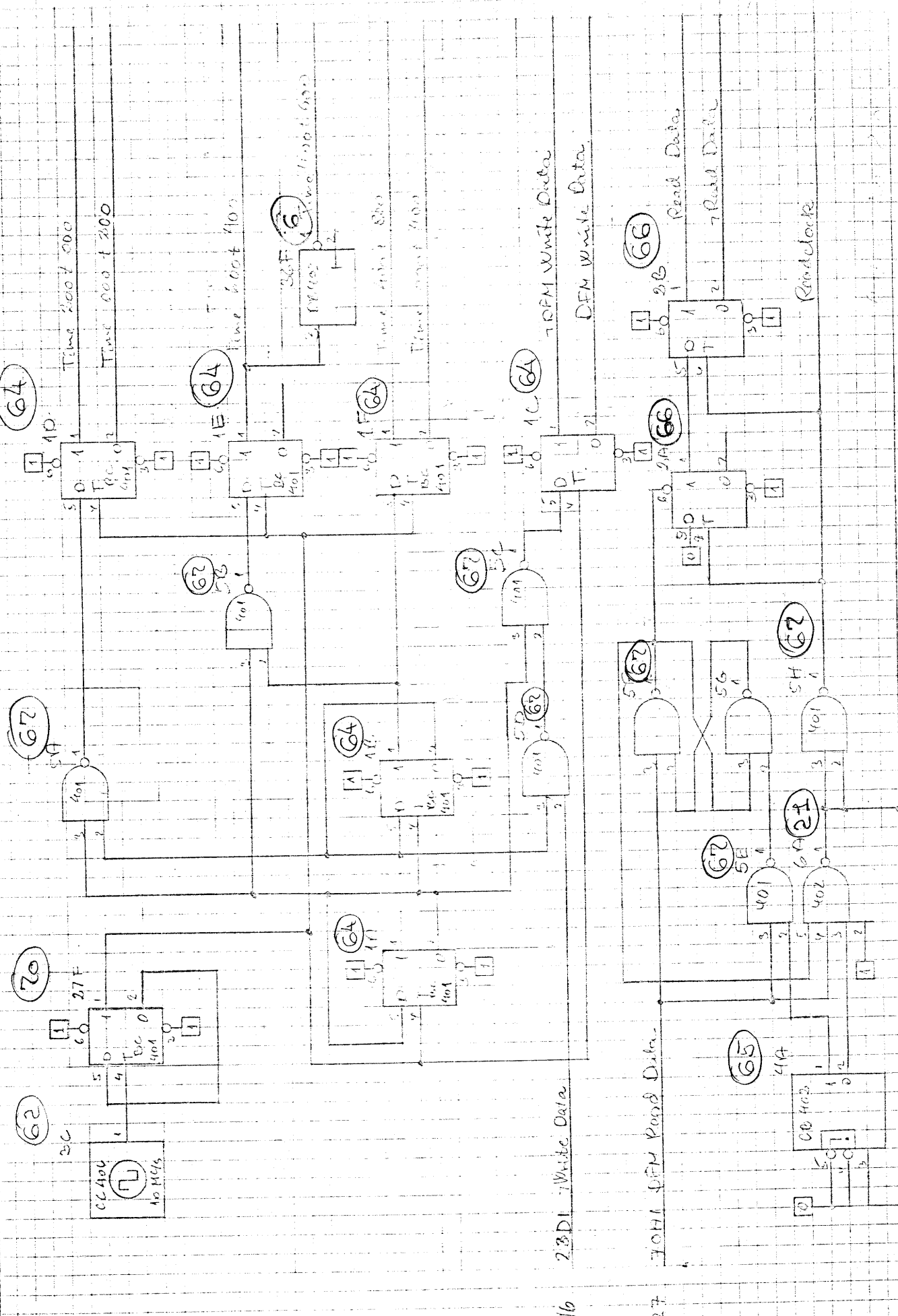


Unit

V-1801

# CLOCK GENERATION, WRITE MODULATOR AND READ DETECTOR

from D-1000



Unit

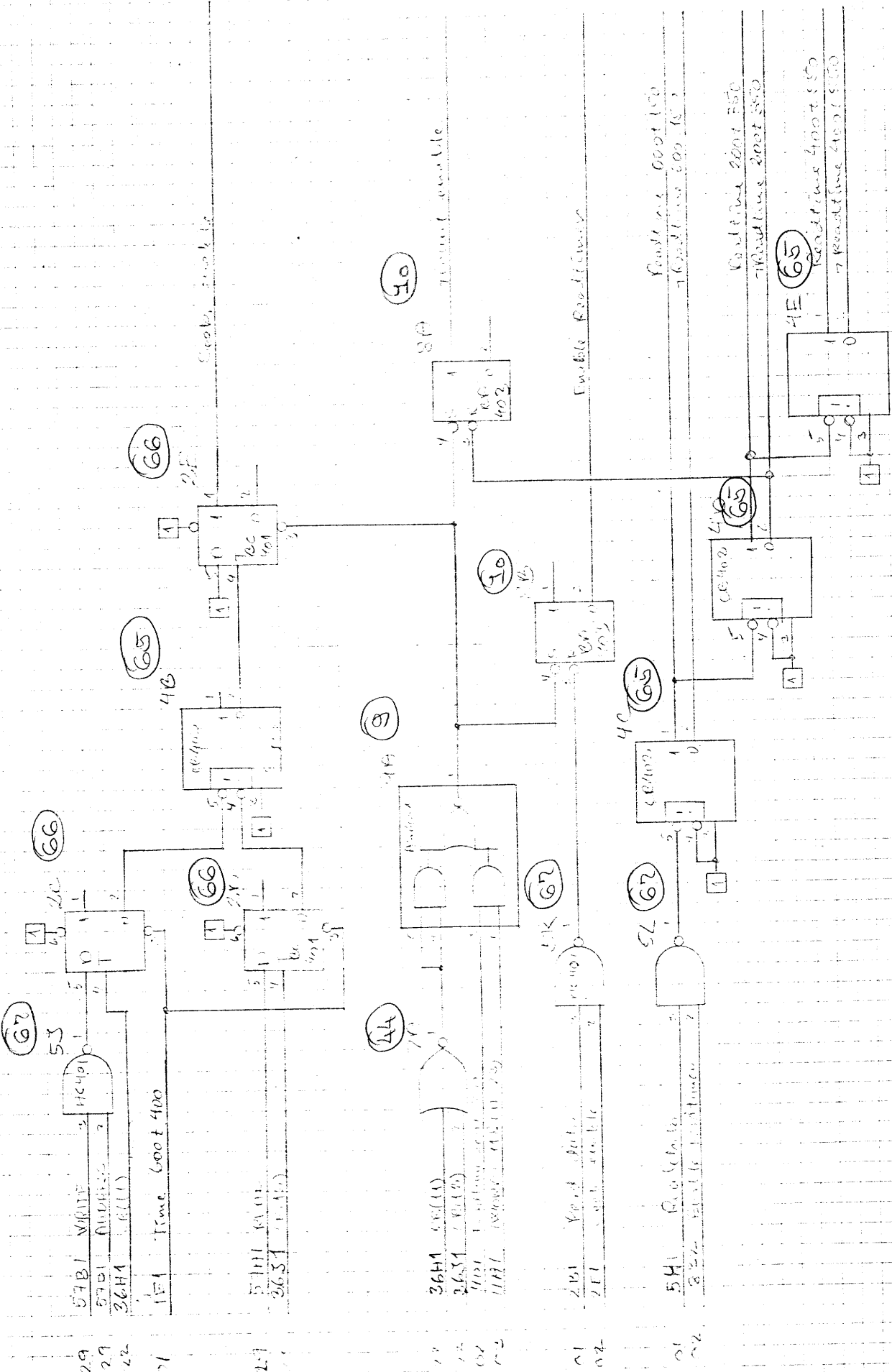
RC4000

Dwg. No.

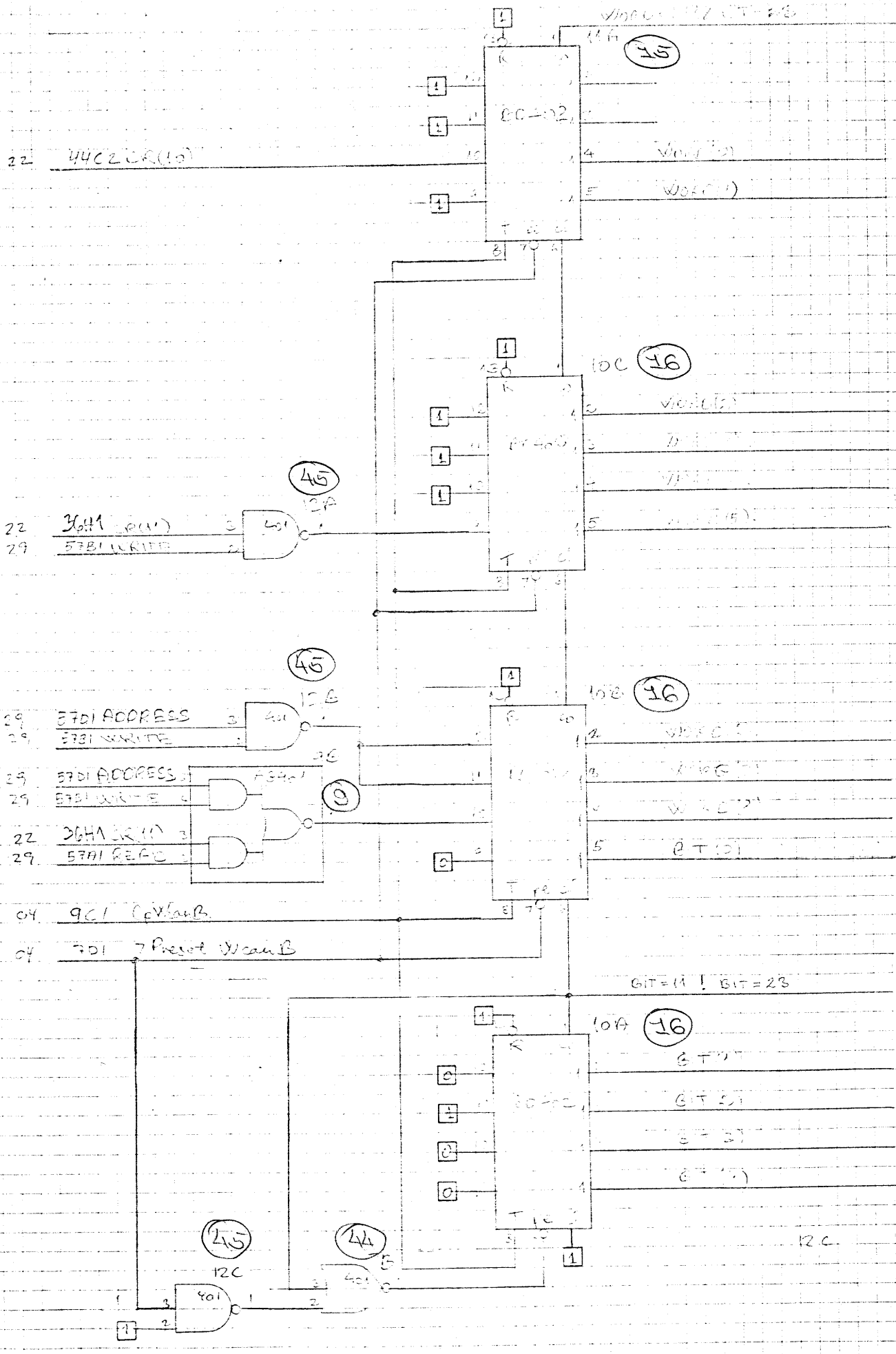
V11802

REF. SINC. CODING  
per Diagram

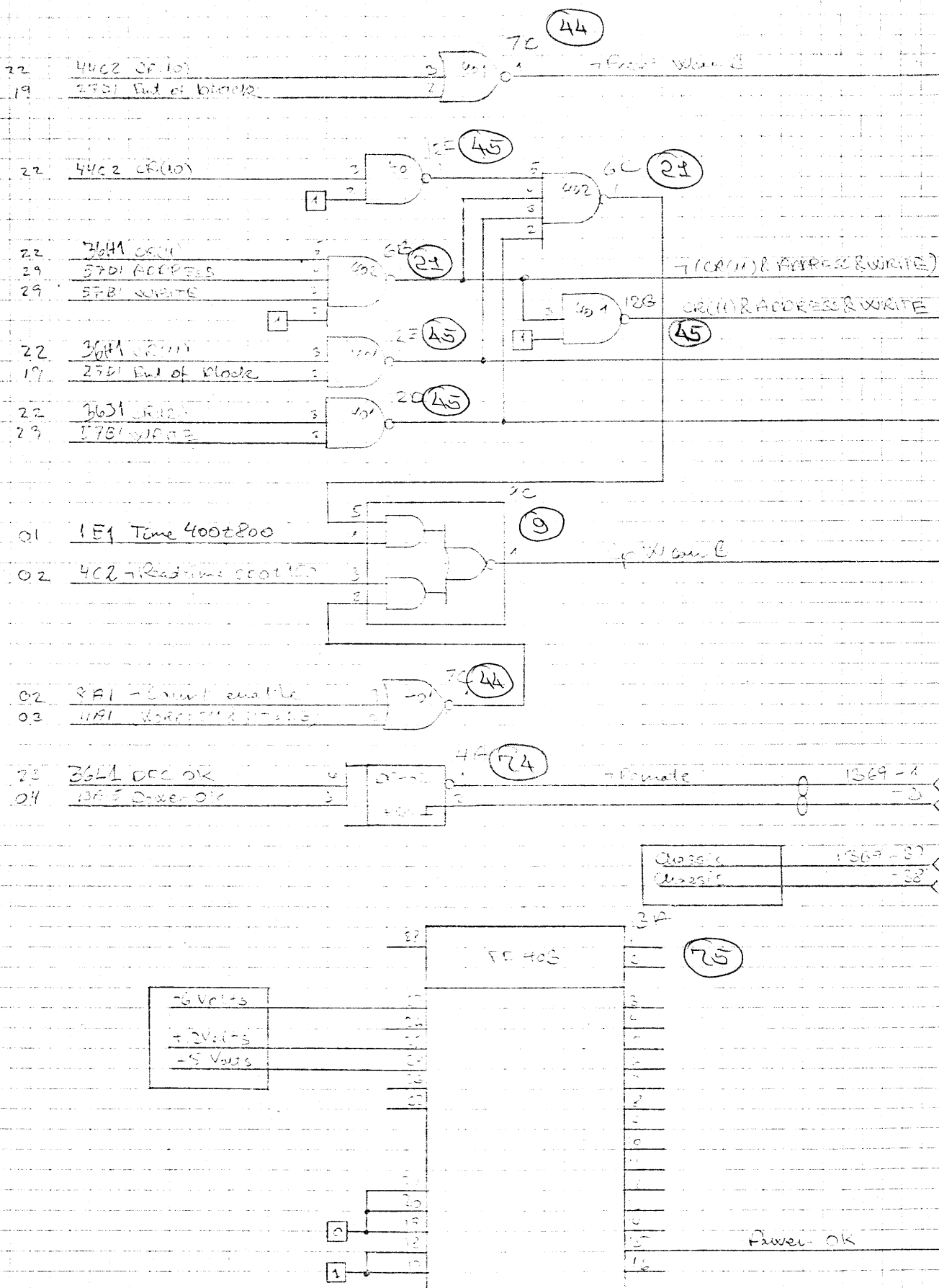
DFC-4



by Dwg. No. \_\_\_\_\_  
 due to ECN \_\_\_\_\_  
 Replaces Dwg. No. \_\_\_\_\_  
 Design Check \_\_\_\_\_  
 Dwg. Office \_\_\_\_\_  
 Drawn by \_\_\_\_\_  
 Designed by \_\_\_\_\_  
 A/S REGISTRATION



Unit	RC4000	WORK - 40 BIT COUNTER, WORK 0 3) BIT 0 4)	DRS - 1
Dwg. No.	V11803		



Revised by Dwg. No.



OFC-5





Unit

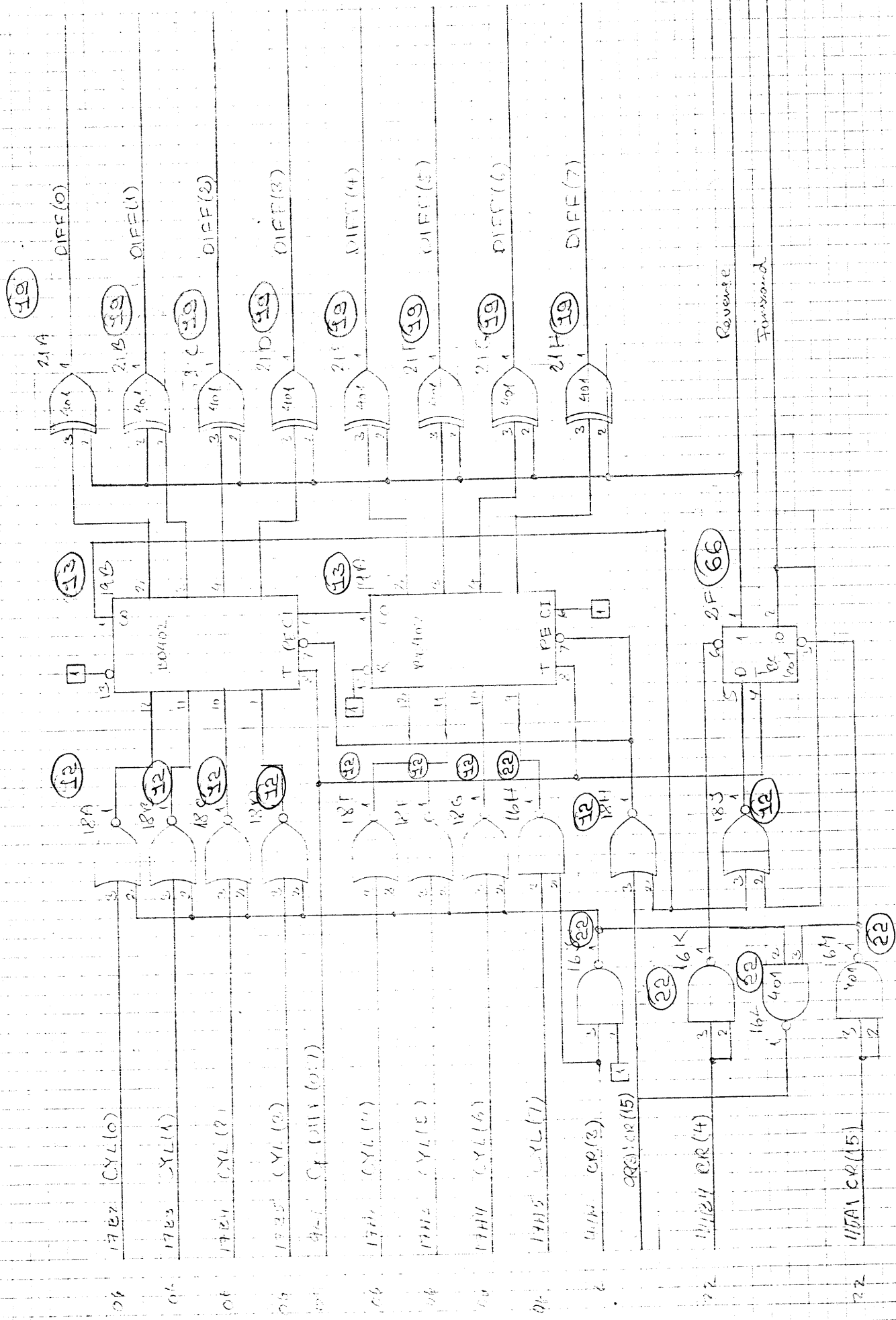
Dwg. No.

VM807

CYLINDER DIFFERENCE, DIFF (0-7)

See C Diagram

DIFF (0-7)



Rep 7 Dwg. No.  
 due to ECN  
 Replaces Dwg. No.  
 Design Check  
 Dwg. Office C  
 Drawn by  
 Designed by  
 A/S REGNE STRALEN

29 55H16 - IO BUS (11)

29 55H17 - IO BUS (10)

29 55H18 - IO BUS (10)

29 55H19 - IO BUS (10)

29 55H20 - IO BUS (10)

29 55H21 - IO BUS (10)

29 55H12 - IO BUS (11)

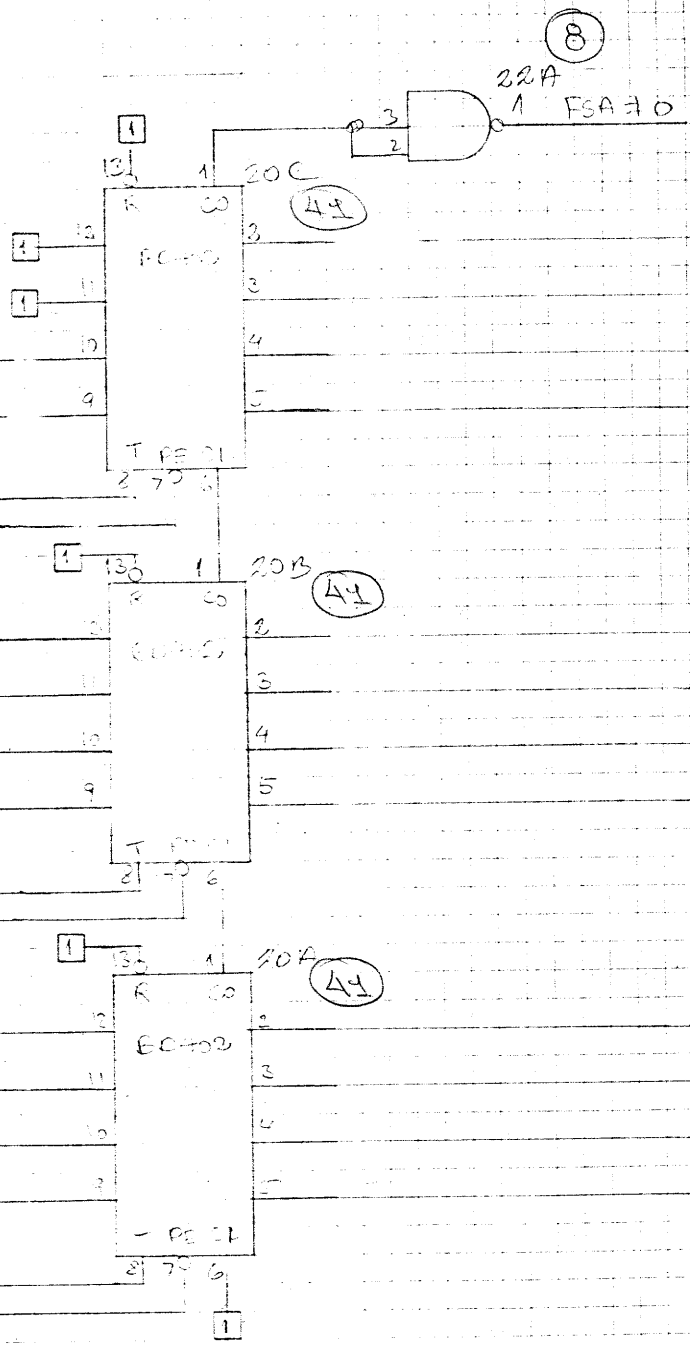
29 55H13 - IO BUS (10)

29 55H14 - IO BUS (10)

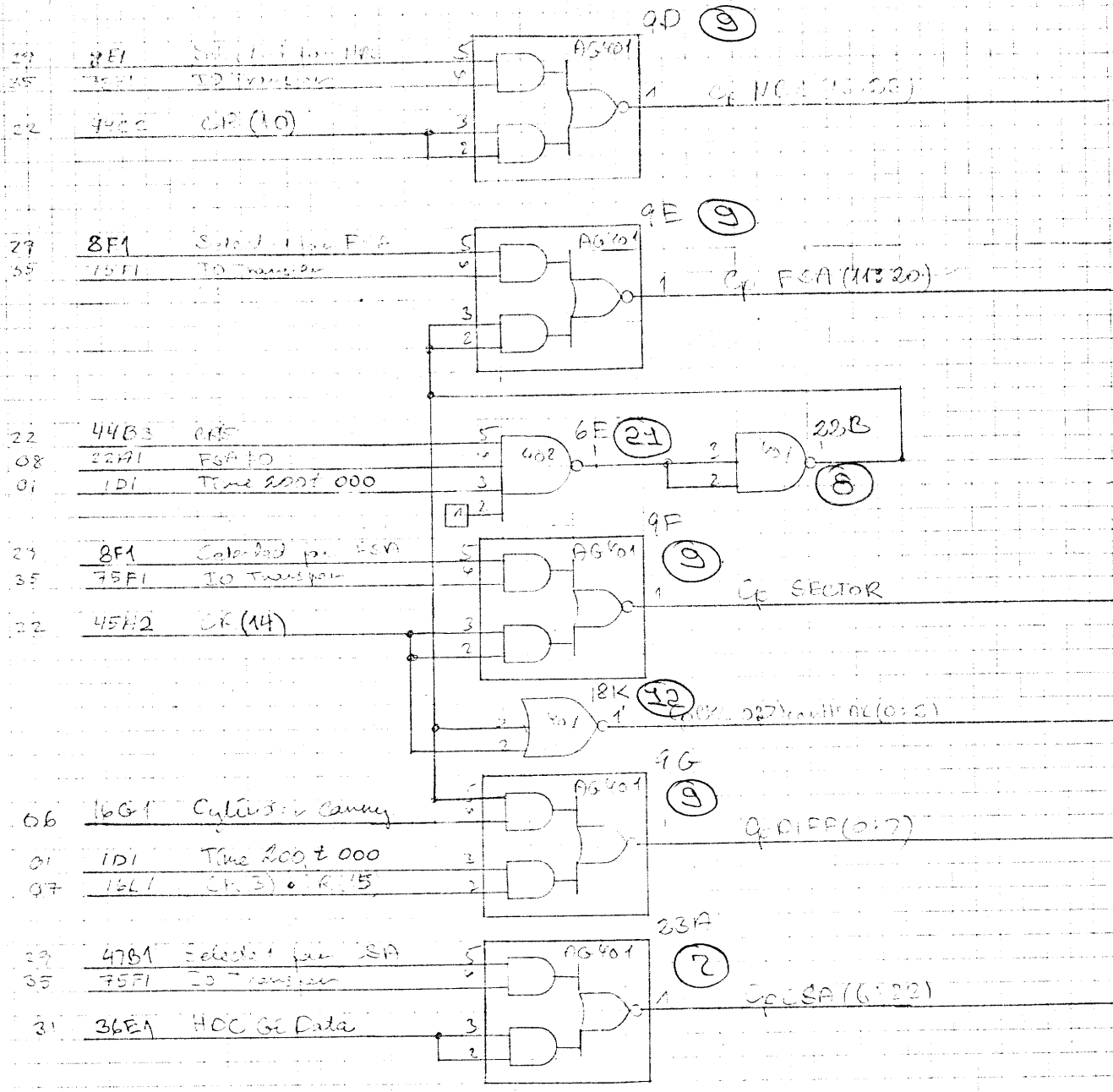
29 55H15 - IO BUS (10)

09 9F1 Co FCP

29 2F3 750121



A/S REGISTRATION  
 Drawn by  
 Designed by  
 Dwg. Office  
 Design Check  
 Replaces Dwg. No.  
 due to ECN  
 by Dwg. No.



Unit	CLOCKPULSES FOR NBS, FSA, SECTOR, CYL, HEAD, DIFF AND CSA	DFC - 1
Dwg. No.	V41809	Logic Diagram

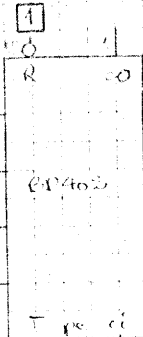
Hand-drawn schematic of a 74181 ALU circuit. The ALU is a large rectangle with inputs 1, 2, 3, 4, 5 on the right and outputs 1, 2, 3, 4, 5 on the left. A 'T' input is at the bottom. A '24A' label is above the ALU. A circled '42' is to the right. A '1' in a box is at the top left, connected to input 1. A '1' in a box is at the bottom right, connected to output 1.

37 DOI: CpNES (15:25)

Unit K0400	NUMBER OF SEGMENTS, NRC (15123)	PFL-10
Dwg. No. V11810		

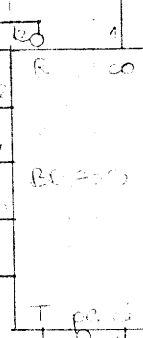
RC 4000  
Unit  
Dwg. No. V11811  
A/S REG  
ENTRALEN  
Designed by  
Drawn by  
Dwg. Office  
Design Check  
Replaces Dwg. No.  
due to EGN  
Revised by Dwg. No.

34 7351 To BUS 6



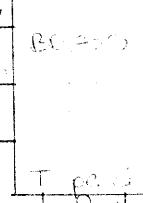
CSA(6)

73F1 To BUS 7



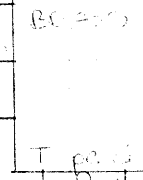
CSA(7)

73G1 To BUS 8



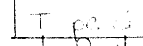
CSA(8)

73H1 To BUS 9



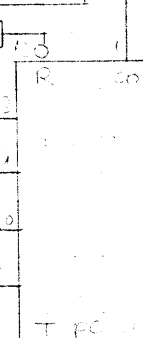
CSA(9)

73J1 To BUS 10



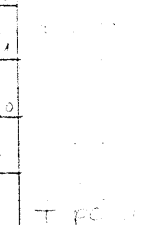
CSA(10)

73K1 To BUS 11



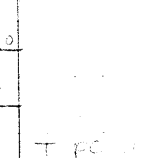
CSA(11)

74A1 To BUS 12



CSA(12)

74B1 To BUS 13



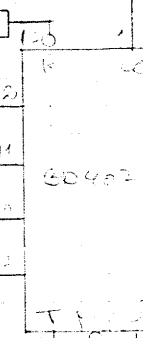
CSA(13)

74C1 To BUS 14



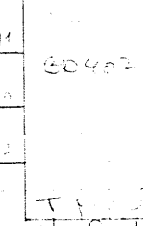
CSA(14)

74D1 To BUS 15



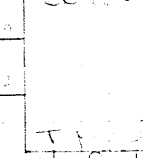
CSA(15)

74E1 To BUS 16



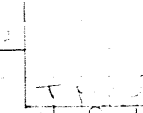
CSA(16)

74F1 To BUS 17



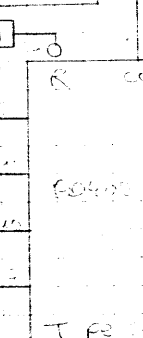
CSA(17)

74G1 To BUS 18



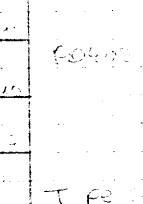
CSA(18)

74H1 To BUS 19



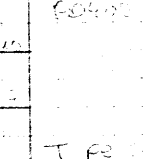
CSA(19)

74J1 To BUS 20



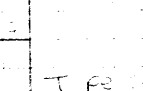
CSA(20)

74K1 To BUS 21



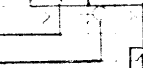
CSA(21)

74L1 To BUS 22

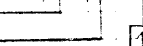


CSA(22)

23H1 To BUS 23



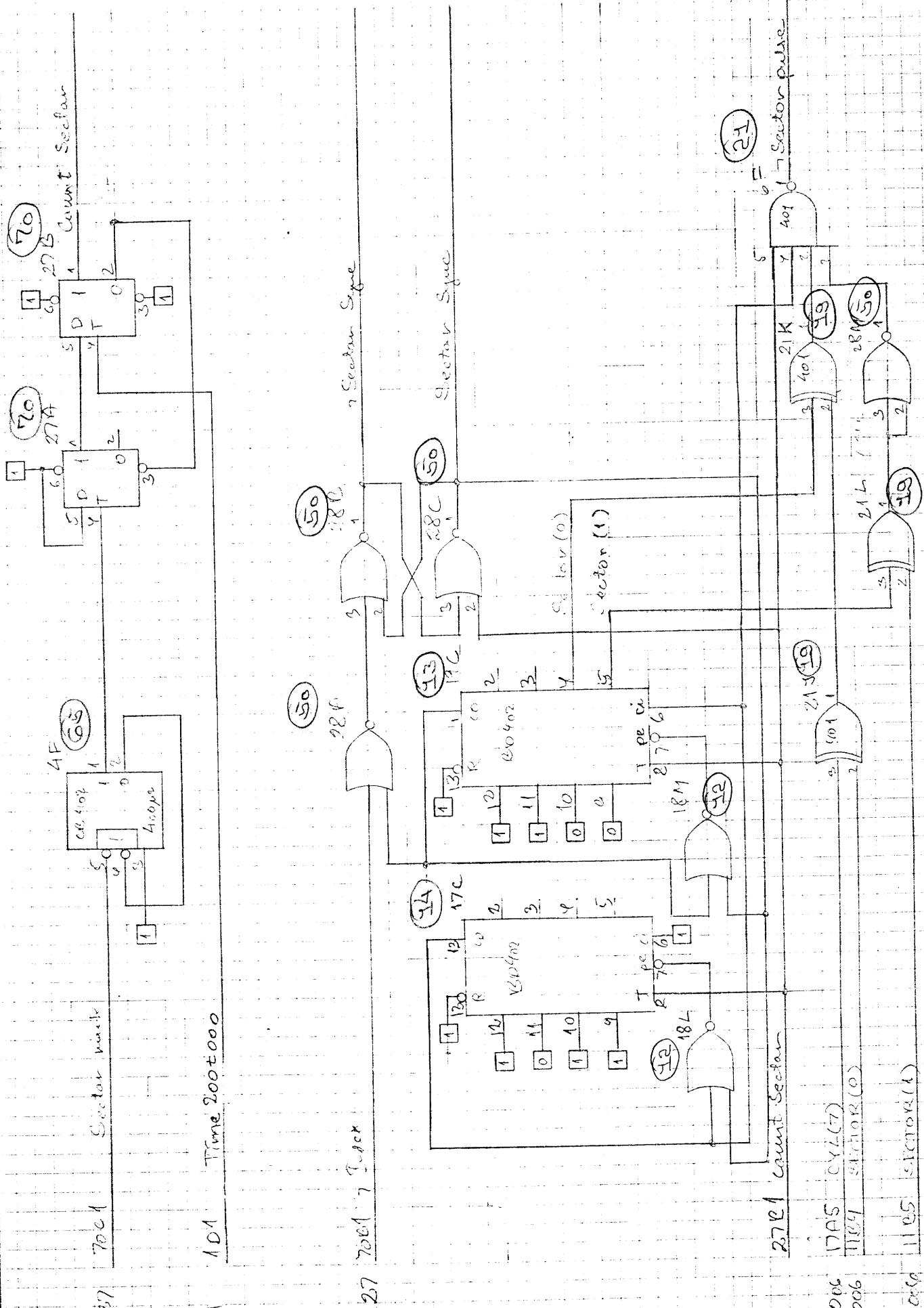
24 30K1 To BUS 24



Unit RC 4000  
Dwg. No. V11811  
CORE CORE ALCKE-2, CSA(6:22)

DFC

57-6-12



05 7kt WIND=253

05 761 WIRE = 509

05 FBI WFO-510

09 901 CnVBS (5:25)

13 20A2 - 8/10/1990

18-2927 Wildlife 3

13 2902 7 White Pine St

13 2901 V. Wphase =

22. 3641 8'11"

72 3631 R-13

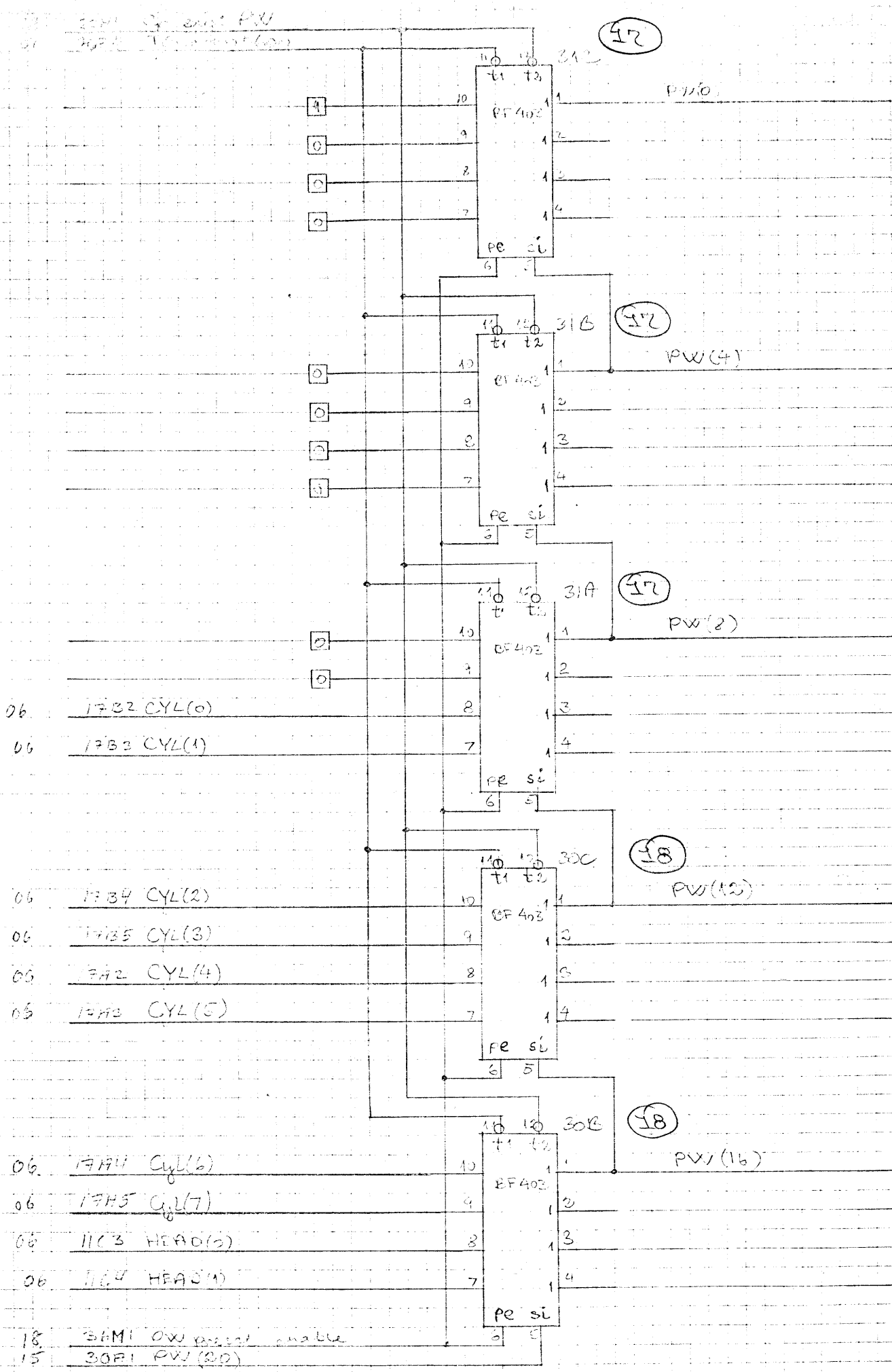
29 37A2 READ

29 3742 READ



DFE-6

Dwg. No.  
V41813

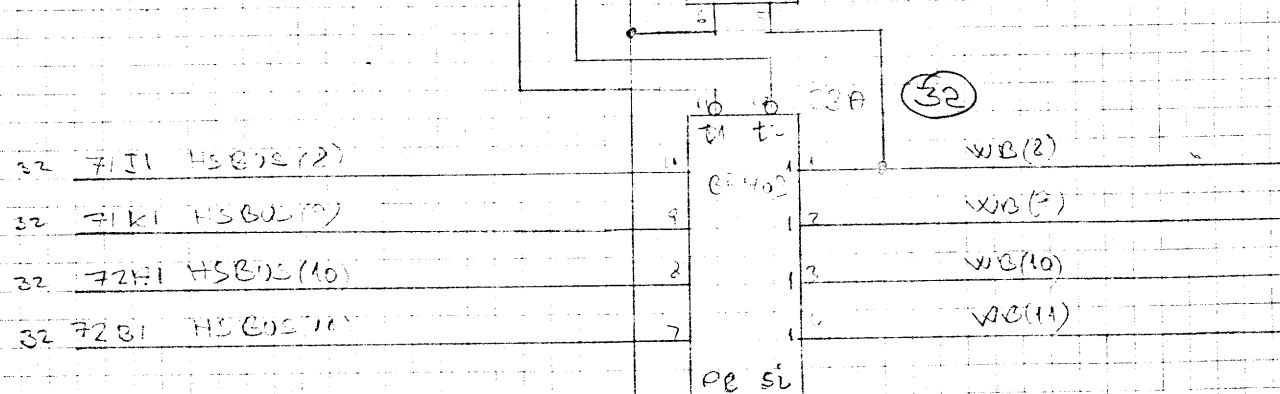
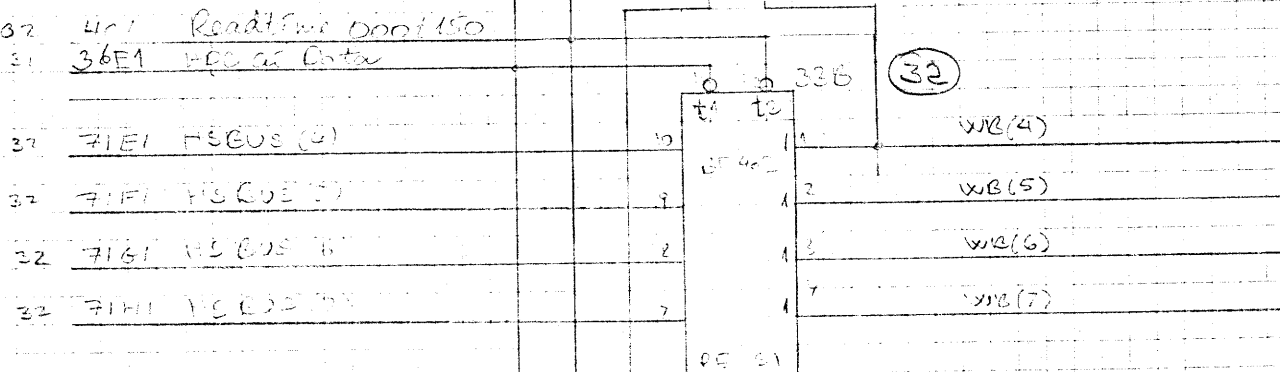
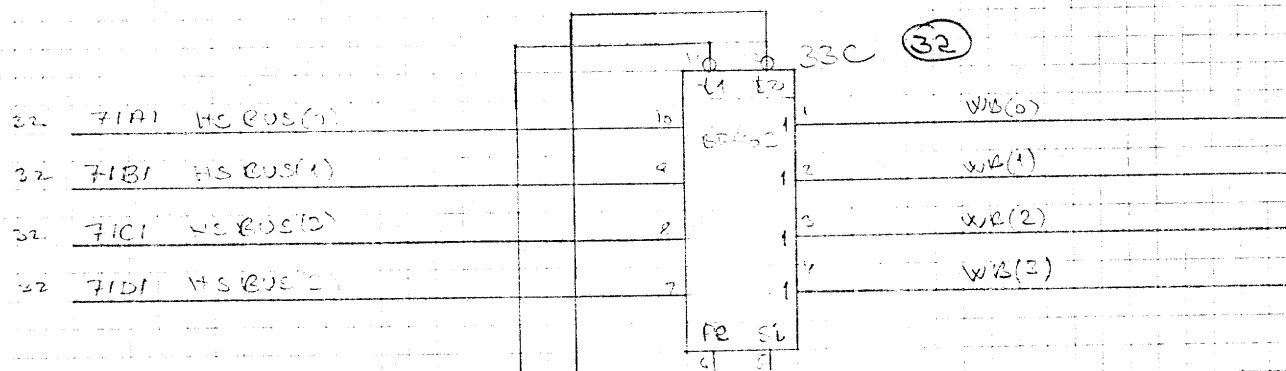
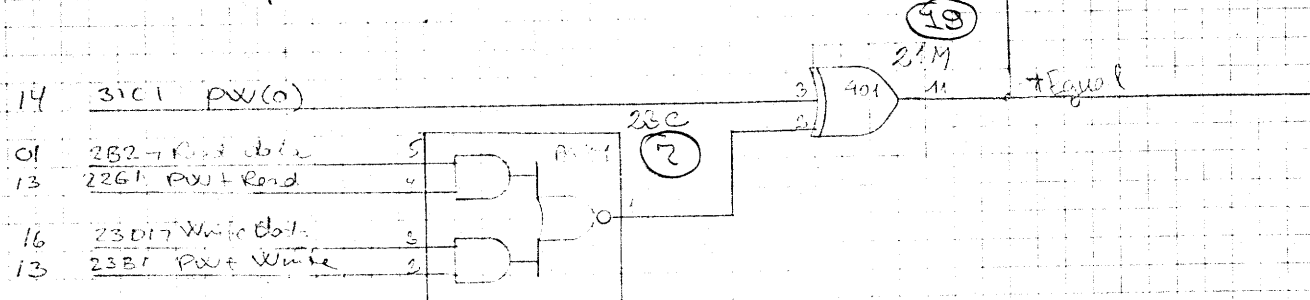
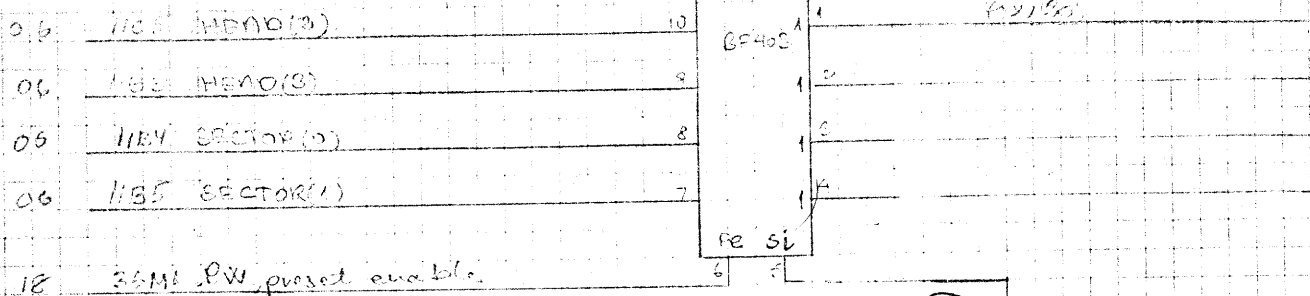


UNITY WORK PW(0:16)

Logic Diagram

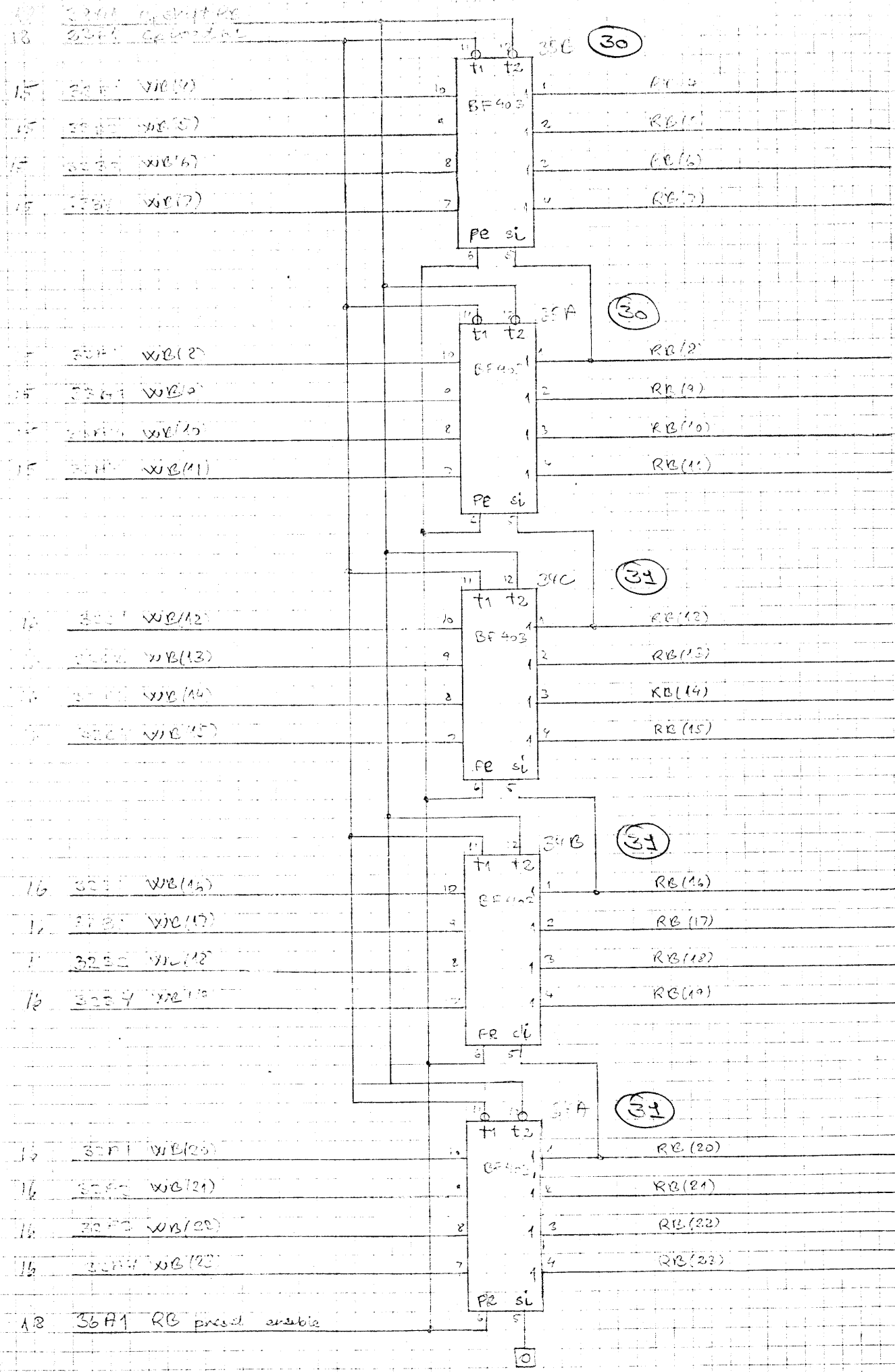
DFC-12





30

Ref. 7 Dwg. No.  
 due to ECN  
 Replaces Dwg. No.  
 Design Check  
 Dwg. Office  
 Drawn by  
 Designed by 12/1/69 FBP  
 A/S REGNL. CONTRALEN

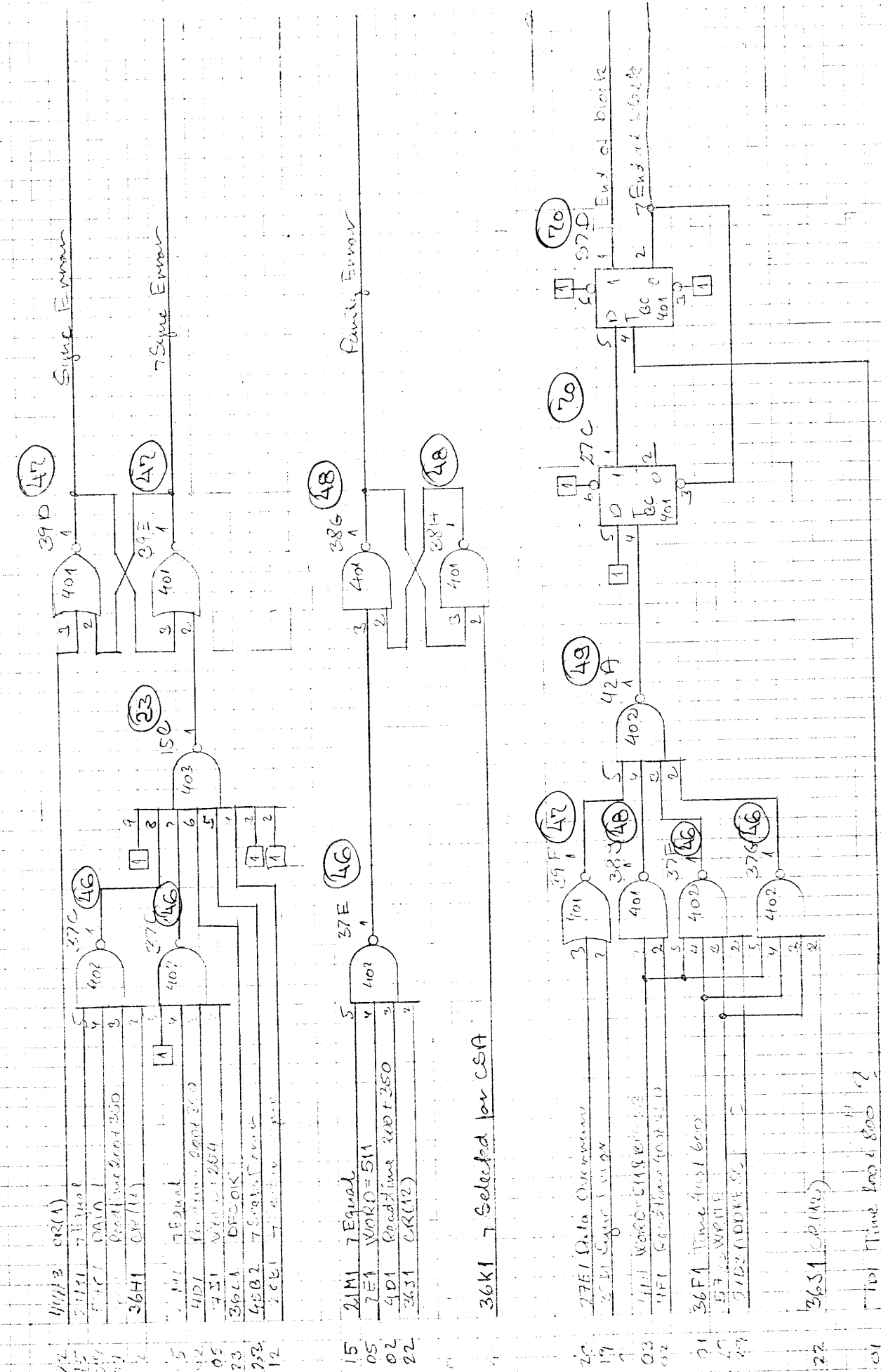




Unit  
R 4000  
Dwg. No.  
V11819

SYNCHRONIZATION AND FAMILY ERROR, END OF BLOCK

Logic Diagram

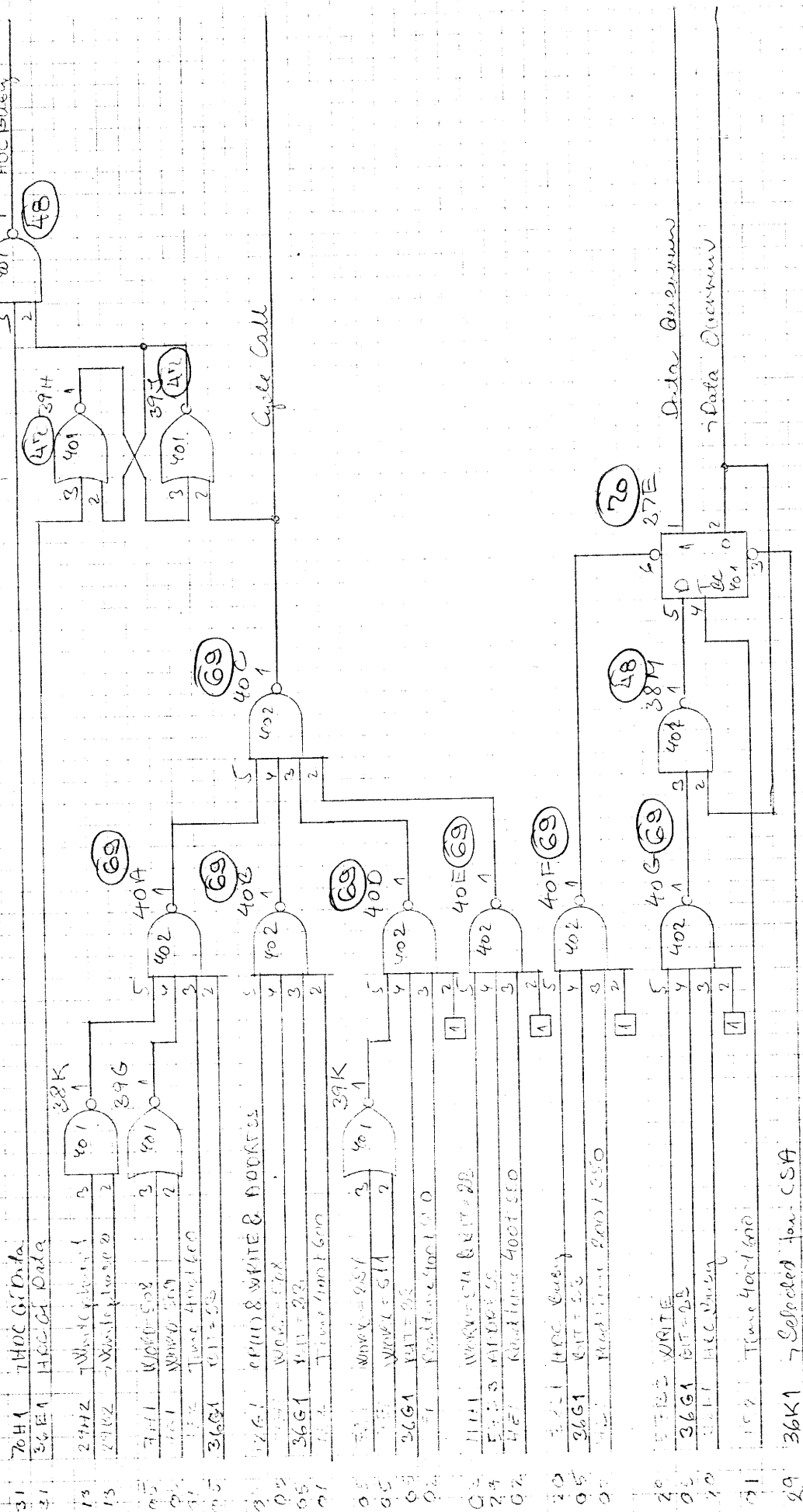


Unit  
RCL4000  
Dwg. No.  
V11820

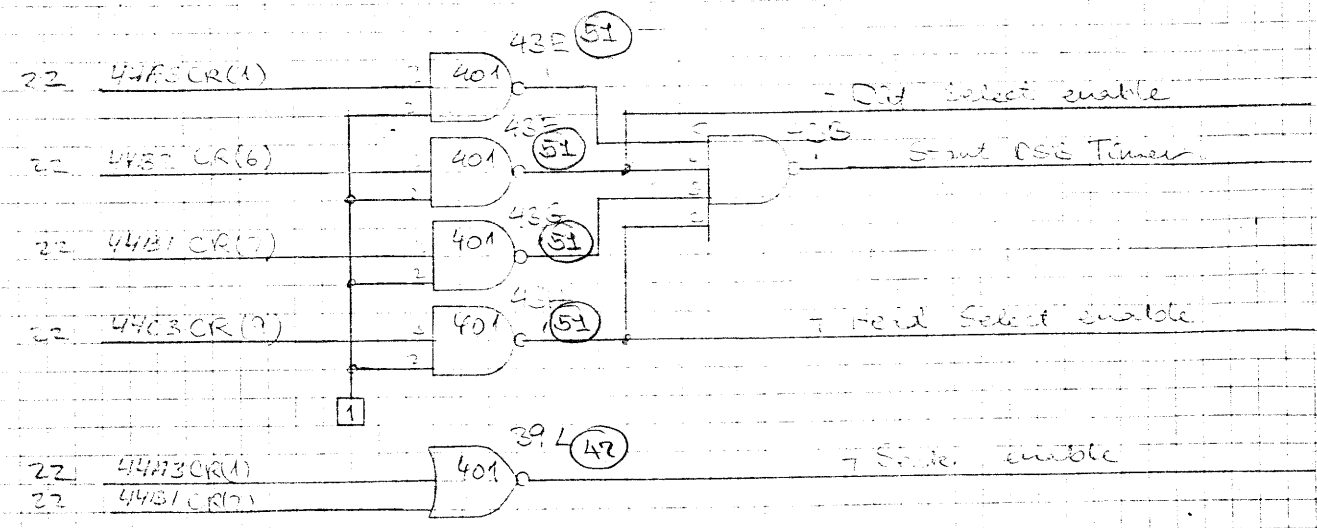
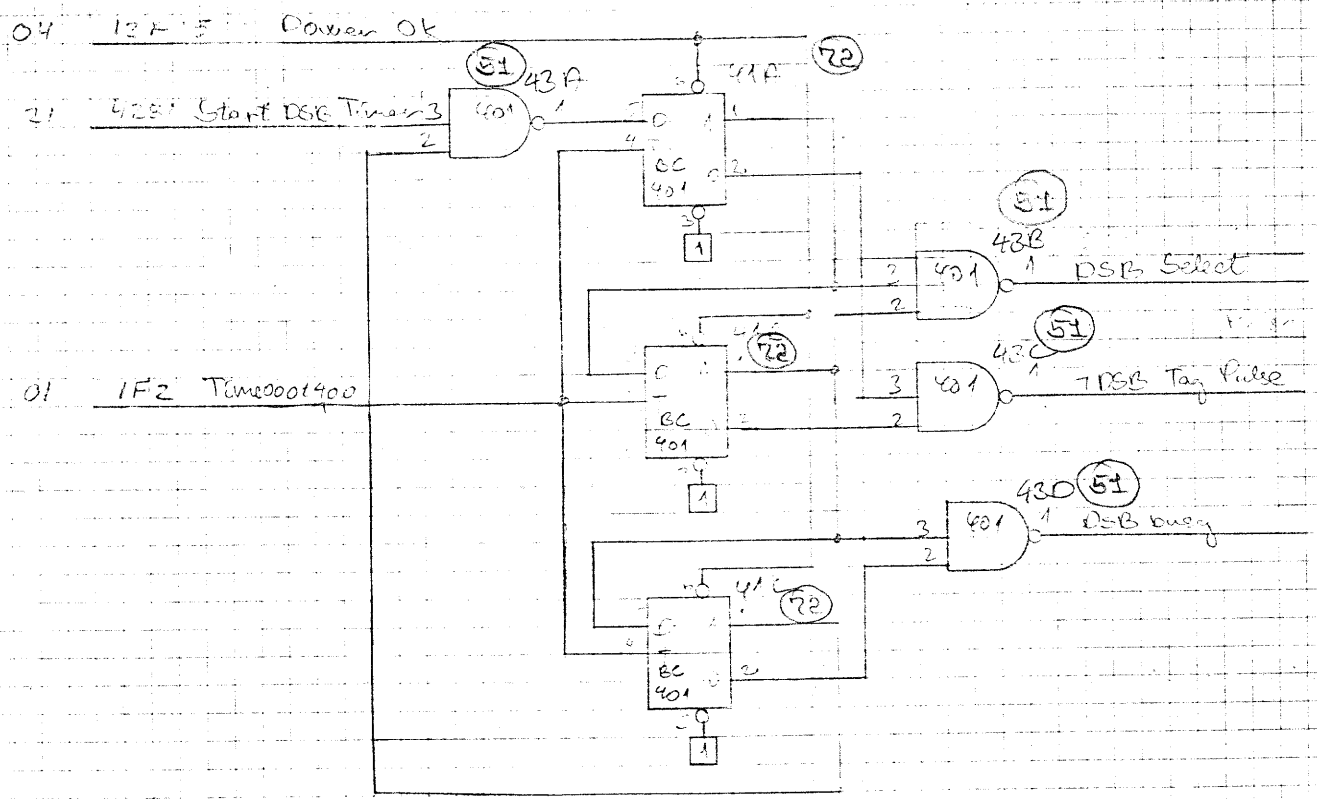
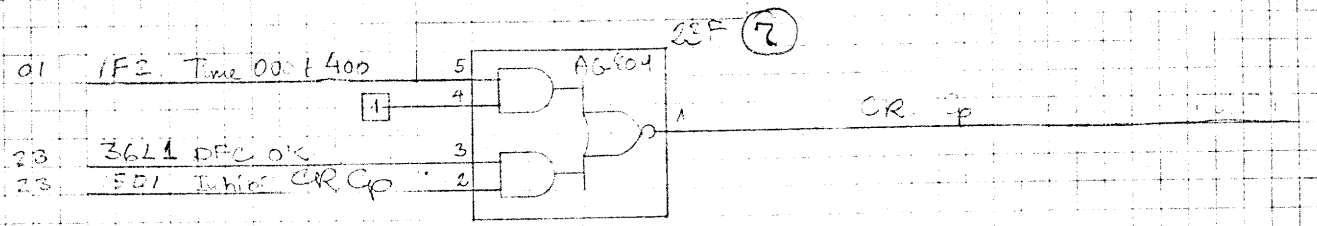
CYCLE CALL AND DATA OVERLAP

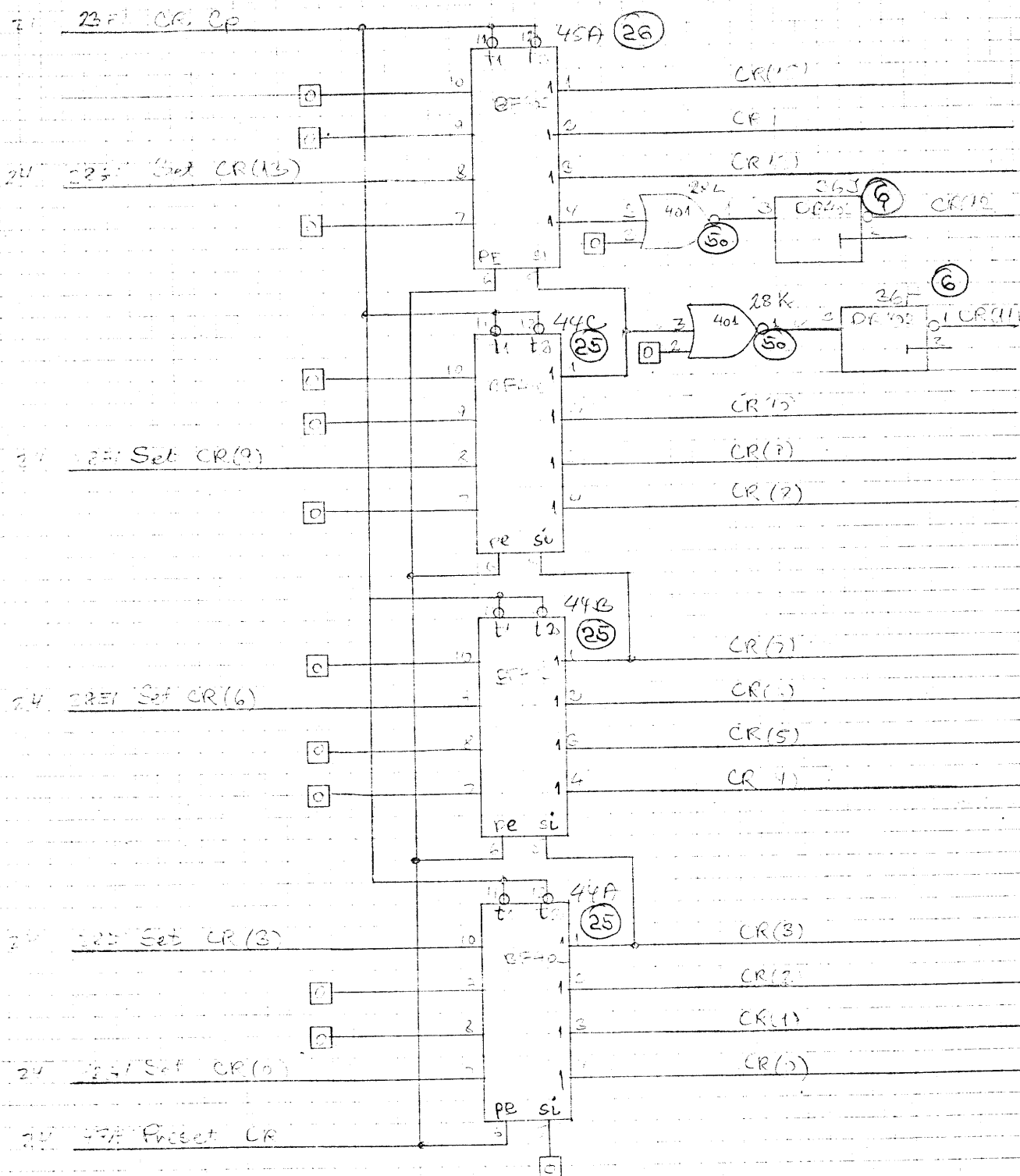
Logic Diagram

DFC-20



Dwg. No. 171167-588  
 Replaces Dwg. No. 171167-588  
 Design Check  
 Dwg. Office  
 Drawn by  
 Designed by  
 171167-588  
 TRALEN  
 A/S REGNES





Unit  
 RC4000  
 Dwg. No.  
 V11822

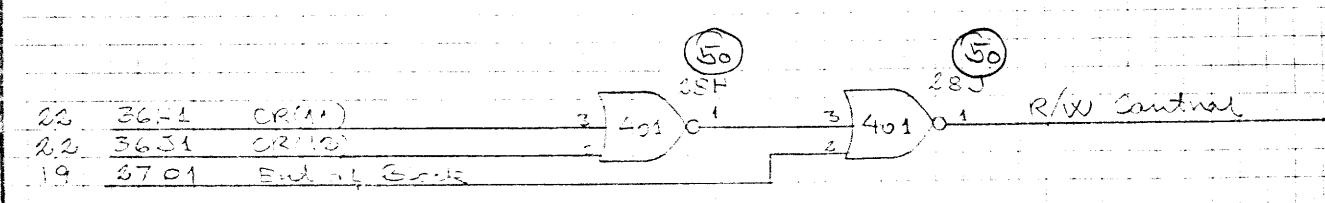
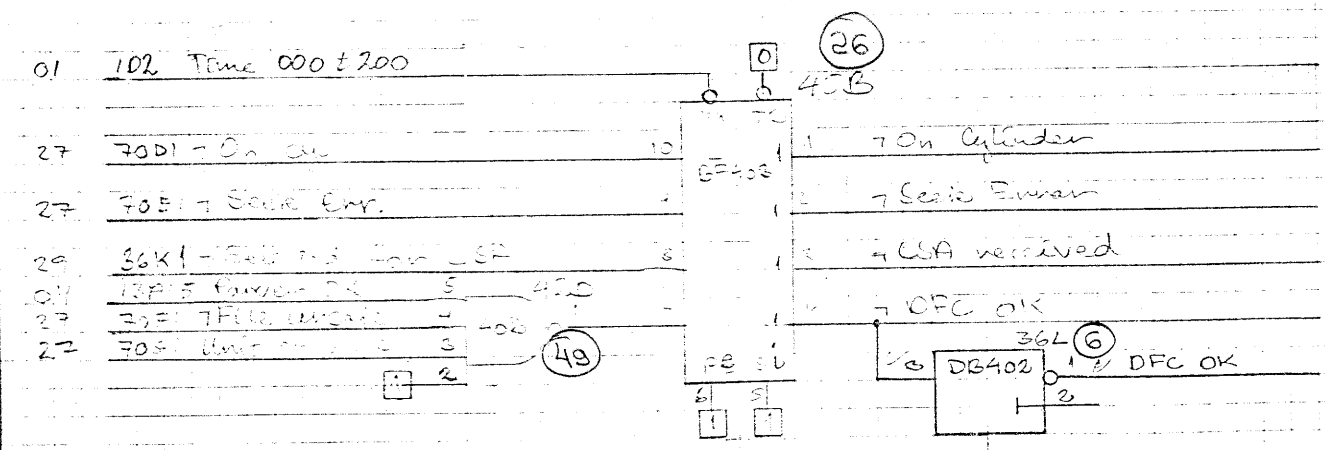
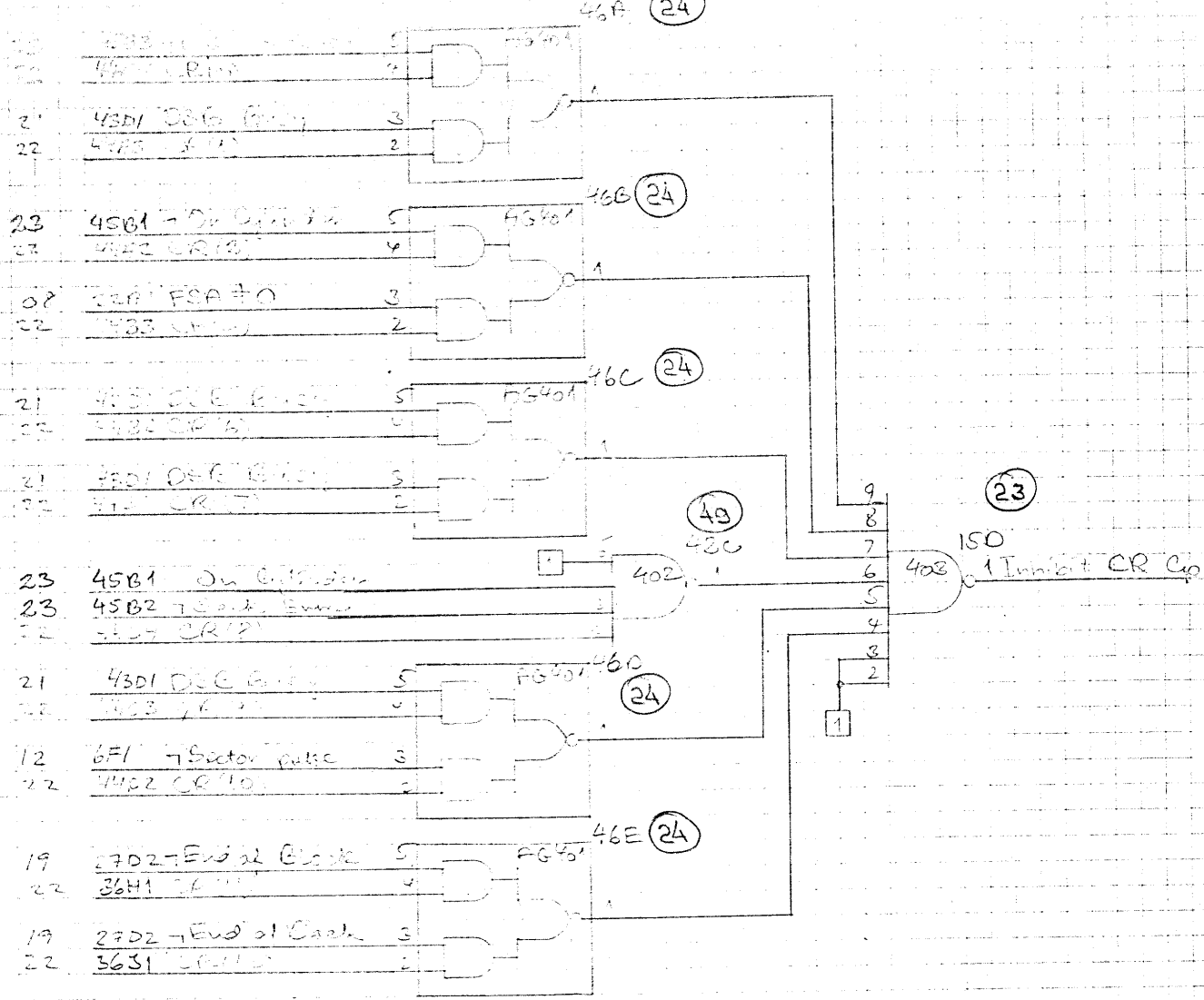
CONTROL REGISTER, CR(0-15)

Logic Diagram

DFC



Unit: R4000  
 Dwg. No. V11823  
 DESIGNED BY: A/S REGENT CENTRALEN  
 Drawn by: [Signature]  
 Dwg. Office: [Signature]  
 Design Check: [Signature]  
 Replaces Dwg. No.: [Signature]  
 due to ECN: [Signature]  
 Rep. 7 Dwg. No.: [Signature]



Unit

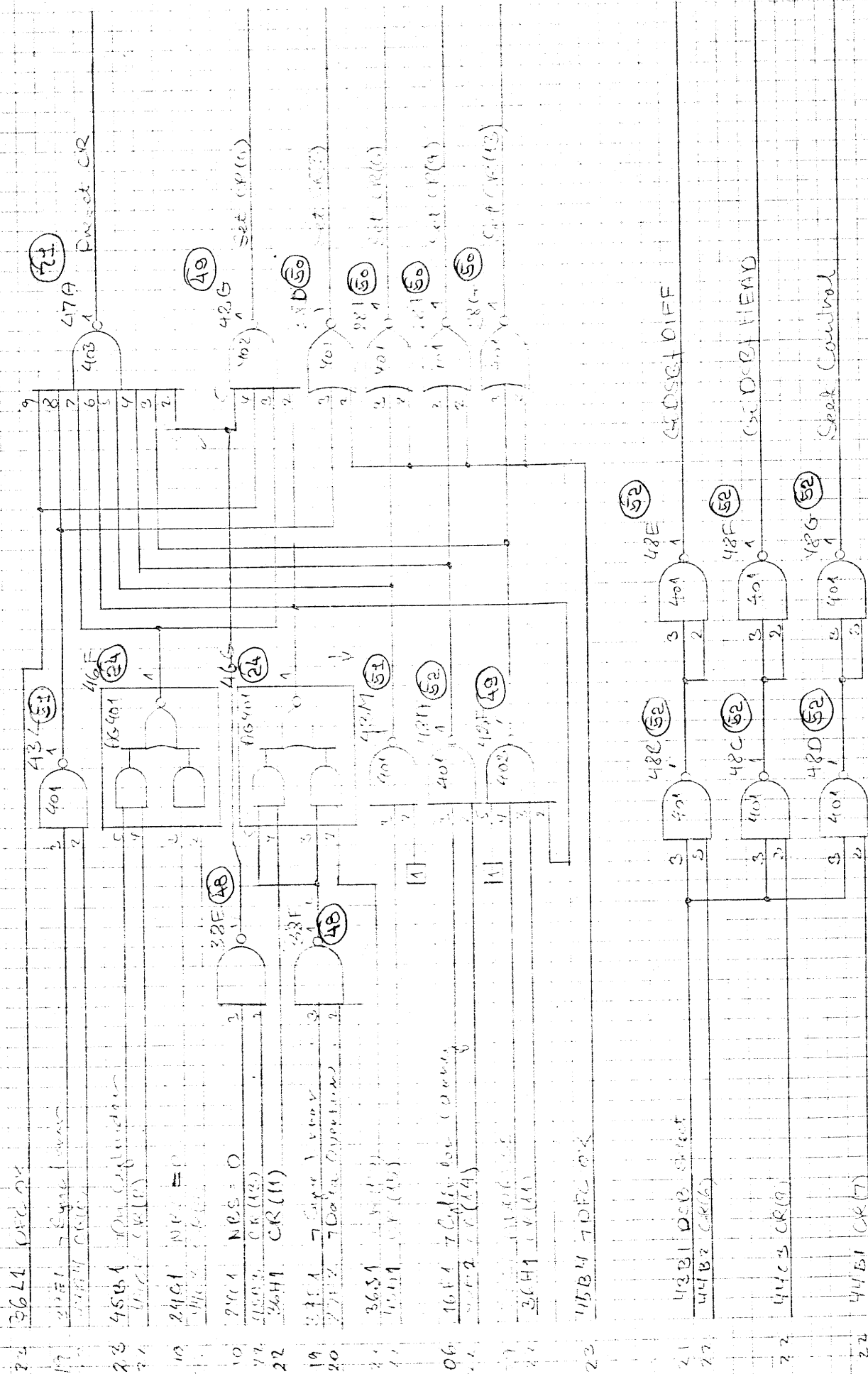
RL4000

Dwg. No.

V11824

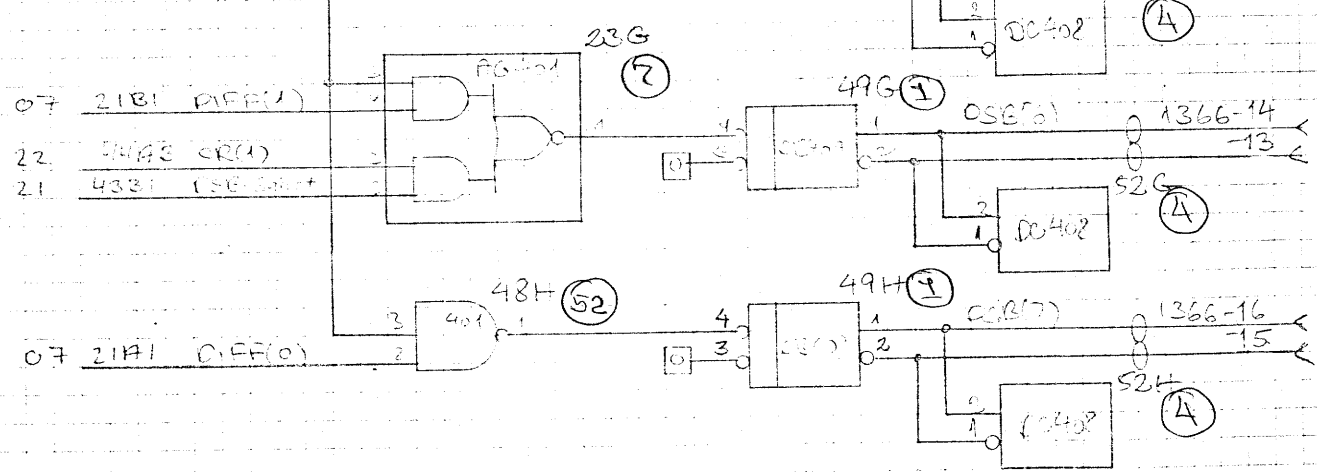
CP UNIT CONTROL

DFC-27

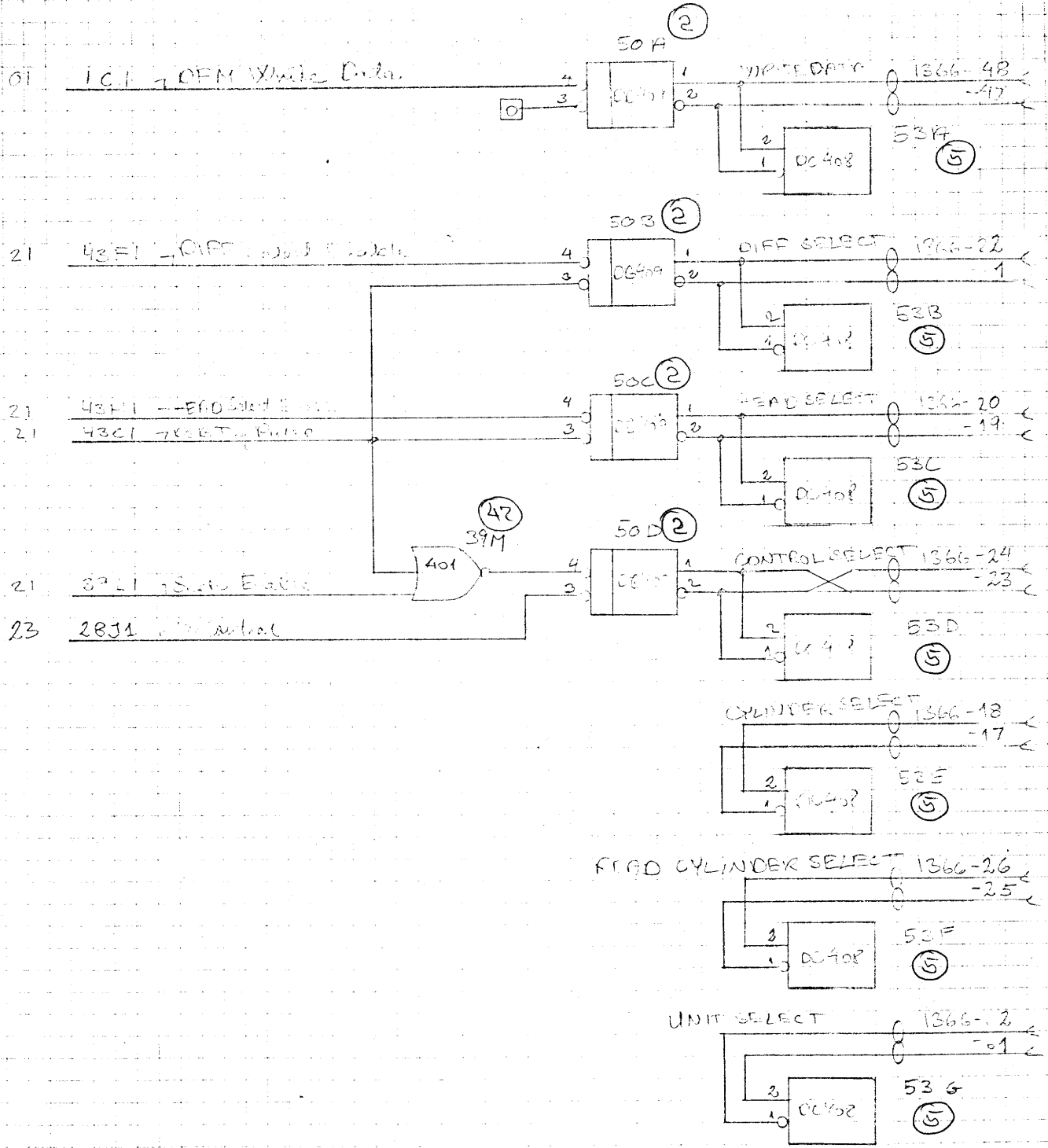


Unit  
 A/S REGNE  
 Designed by  
 271169 FBP  
 Drawn by  
 Dwg. Office  
 Design Check  
 Replaces Dwg. No.  
 due to ECN  
 Rep. 7 Dwg. No.

18	22L1	Write Gate	0	12
06	11B3	HEAD(3)	0	13
07	21H1	DIFF(2)	0	14
18	22C1	Read Gate	0	15
06	11C5	HEAD(2)	0	16
07	21G1	DIFF(6)	0	17
07	2F2	Forward	0	18
06	11C4	HEAD(1)	0	19
07	2F1	DIFF	0	20
06	11C3	HEAD(0)	0	21
07	21E1	DIFF(7)	0	22
18	22C1	Erase Pulse	0	23
07	21D1	DIFF(3)	0	24
07	2F1	Reverse	0	25
07	21C1	DIFF(2)	0	26
24	43B1	Seek Control	0	27
23	43J3	R/W Control	0	28
24	43F1	ADDRESS HEAD	0	29
24	43E1	ADDRESS DIFF	0	30



Unit  
 A/S REGNE CENTRALE  
 Dwg. No.  
 V11826  
 Designed by  
 2/11/68 J.R.  
 Drawn by  
 Dwg. Office  
 Design Check  
 Replaces Dwg. No.  
 due to EGN  
 Rep. of Dwg. No.



SIGNALS FROM CONTROLLER TO PSD  
 Logic Diagram

DFC-026

Drawn by

Replaces Dwg. No.

Design Check

Dwg. Office

Designed by

Unit

RE 4000

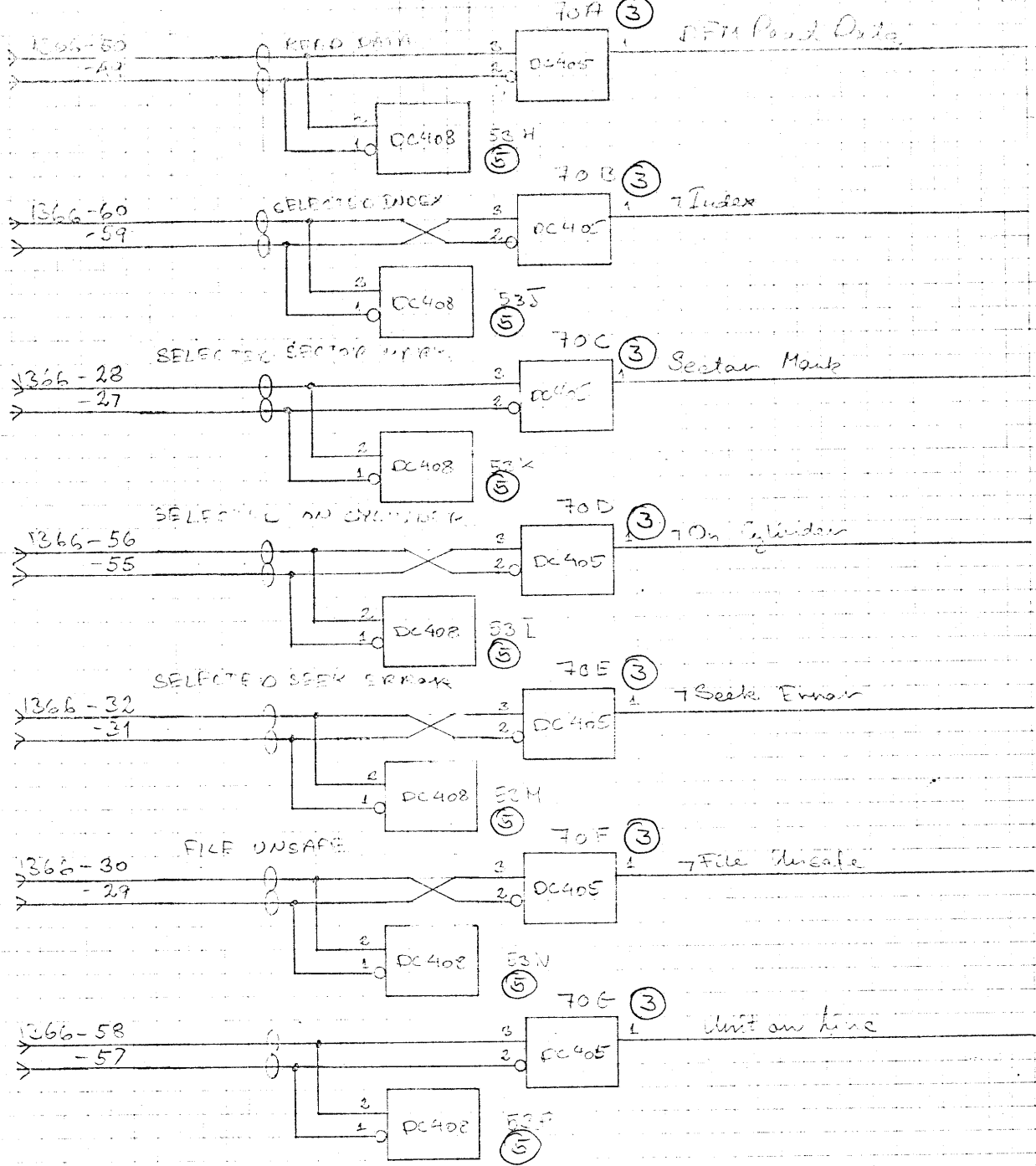
Dwg. No.

V11827

Signals from UCD to Controller

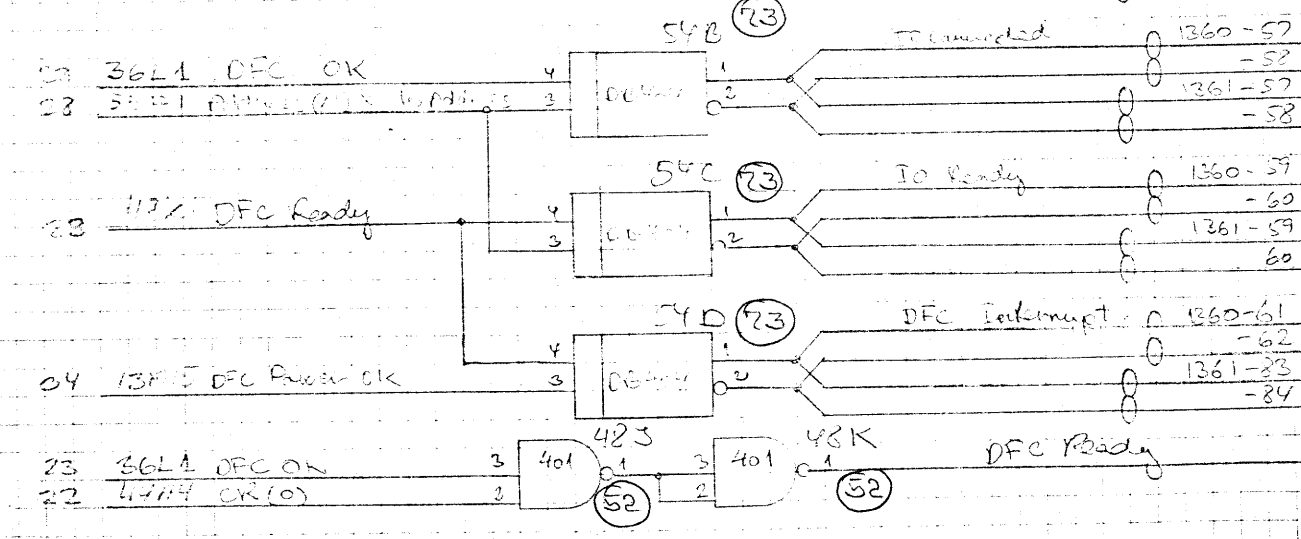
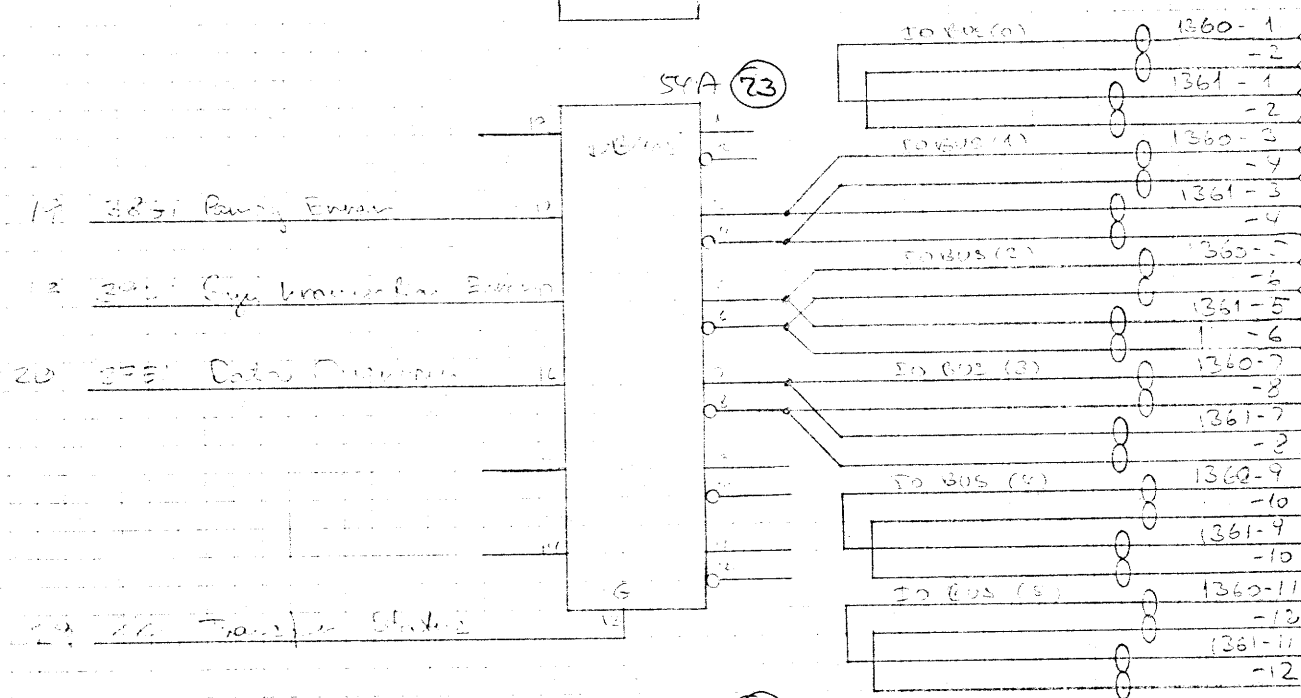
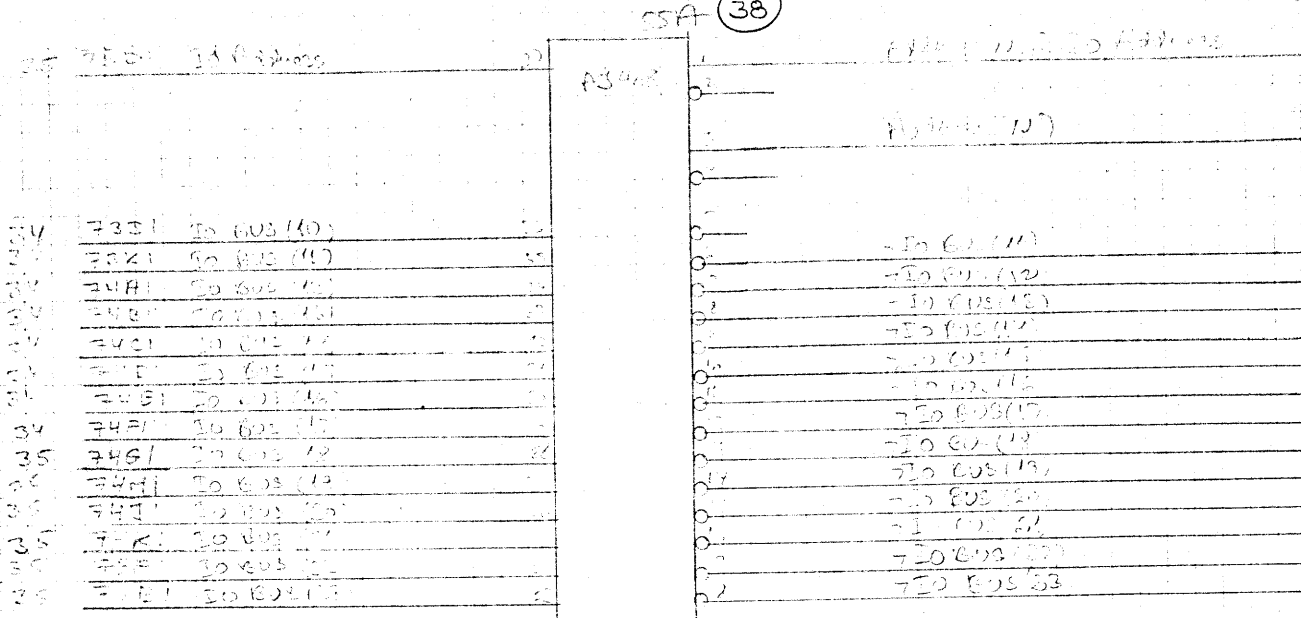
Logic Diagram

DFC-27



0 Volt	1366-86
0 Volt	1366-87
2 V	1366-88
0 Volt	1366-89
0 Volt	1366-90

PC doc: V8 140  
 A/S REGISTRATION  
 Designed by  
 Drawn by  
 Design Check  
 Replaces Dwg. No.  
 due to EGN  
 by Dwg. No.



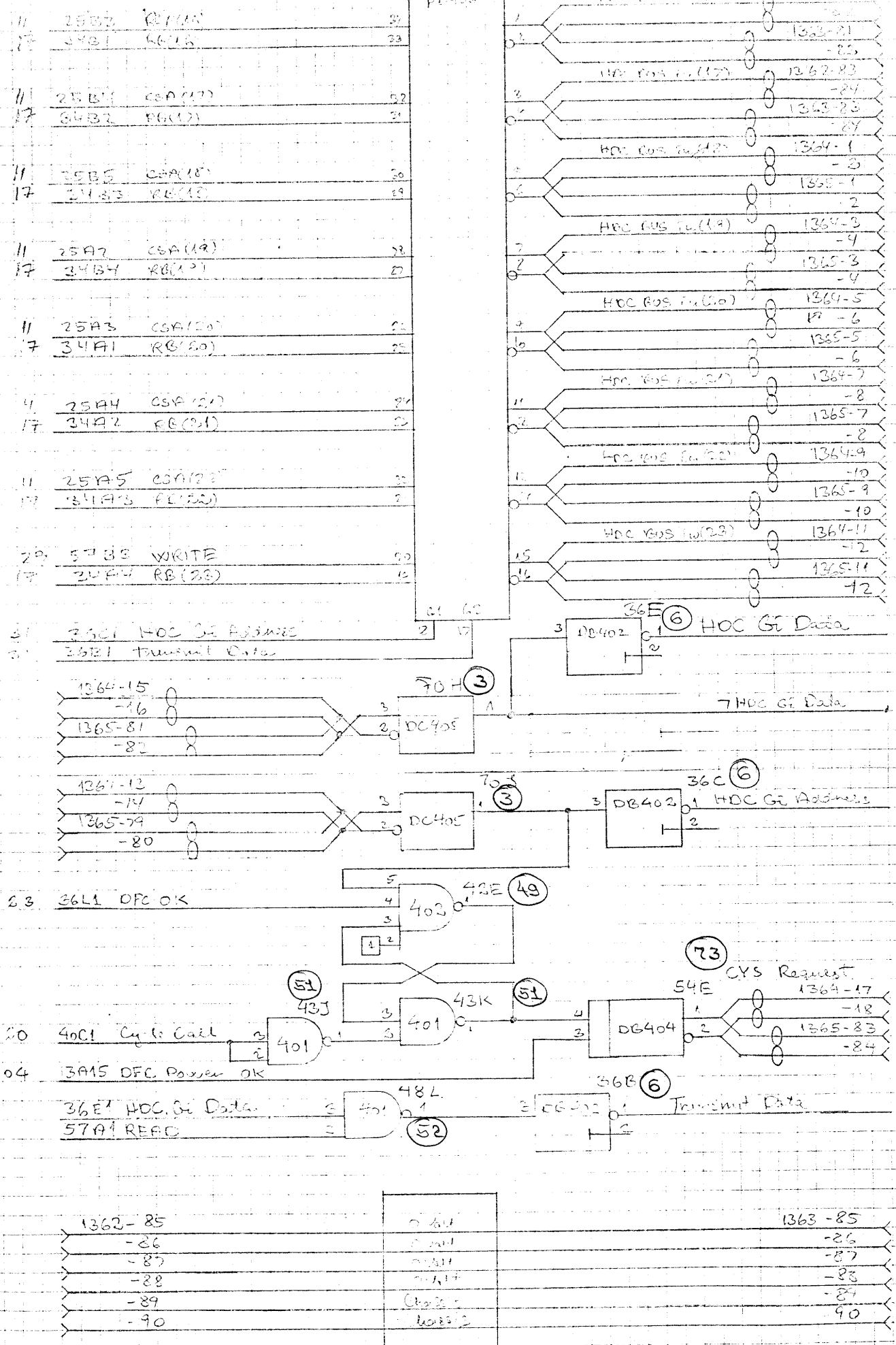
Unit	DFC-22	
Dwg. No.	V11828	
Logic Diagram		

DECEMBER





Rep. by Dwg. No. \_\_\_\_\_  
 due to ECN \_\_\_\_\_  
 Replaces Dwg. No. \_\_\_\_\_  
 Design Check \_\_\_\_\_  
 Dwg. Office C. \_\_\_\_\_  
 Drawn by \_\_\_\_\_  
 Designed by 181169 F.B.P.  
**AIS REGNE CENTRALEN**  
 Unit: RC 4000  
 Dwg. No. V11831

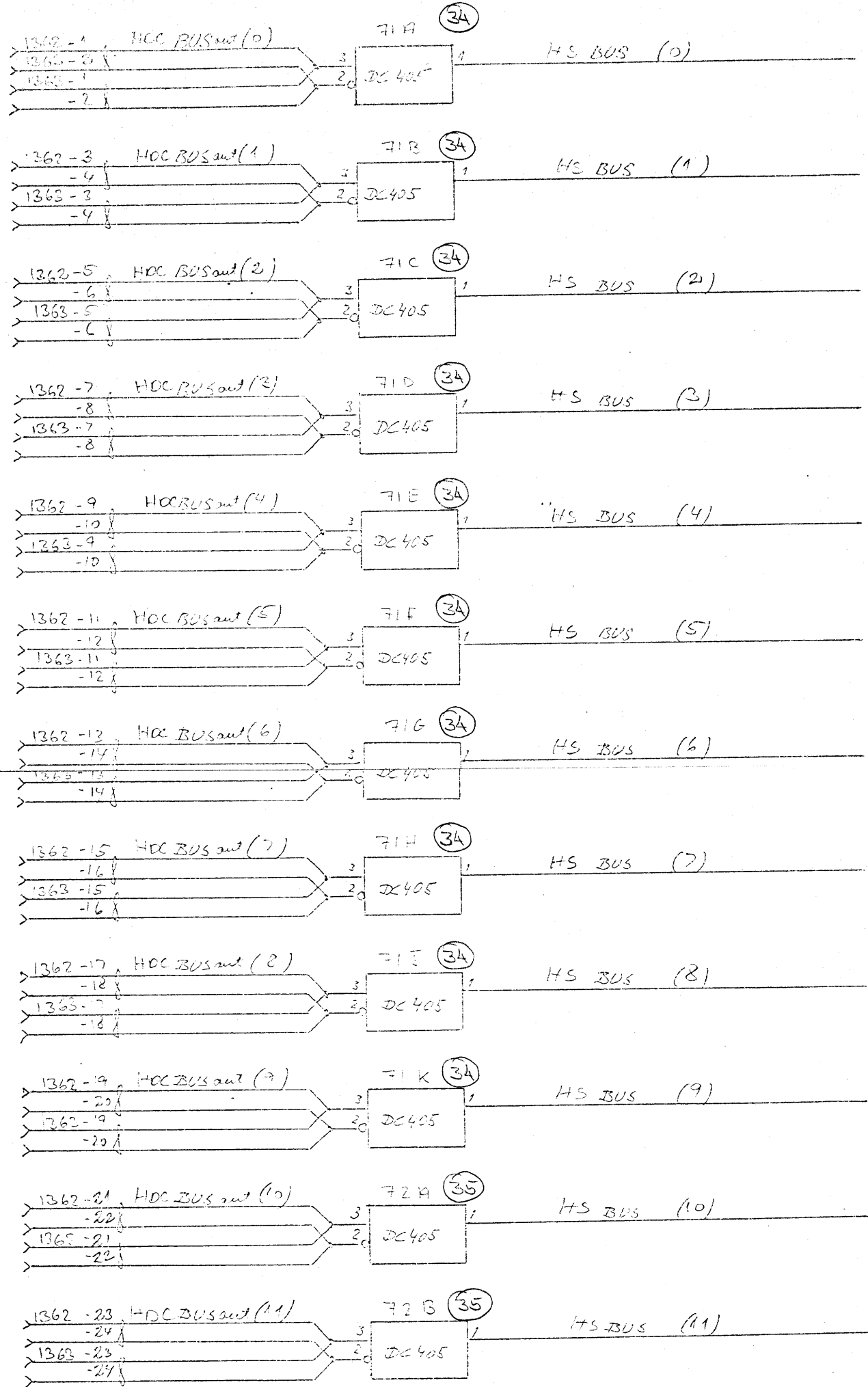


HOC BUS (16:25) and HOC Control  
 Logic Diagram

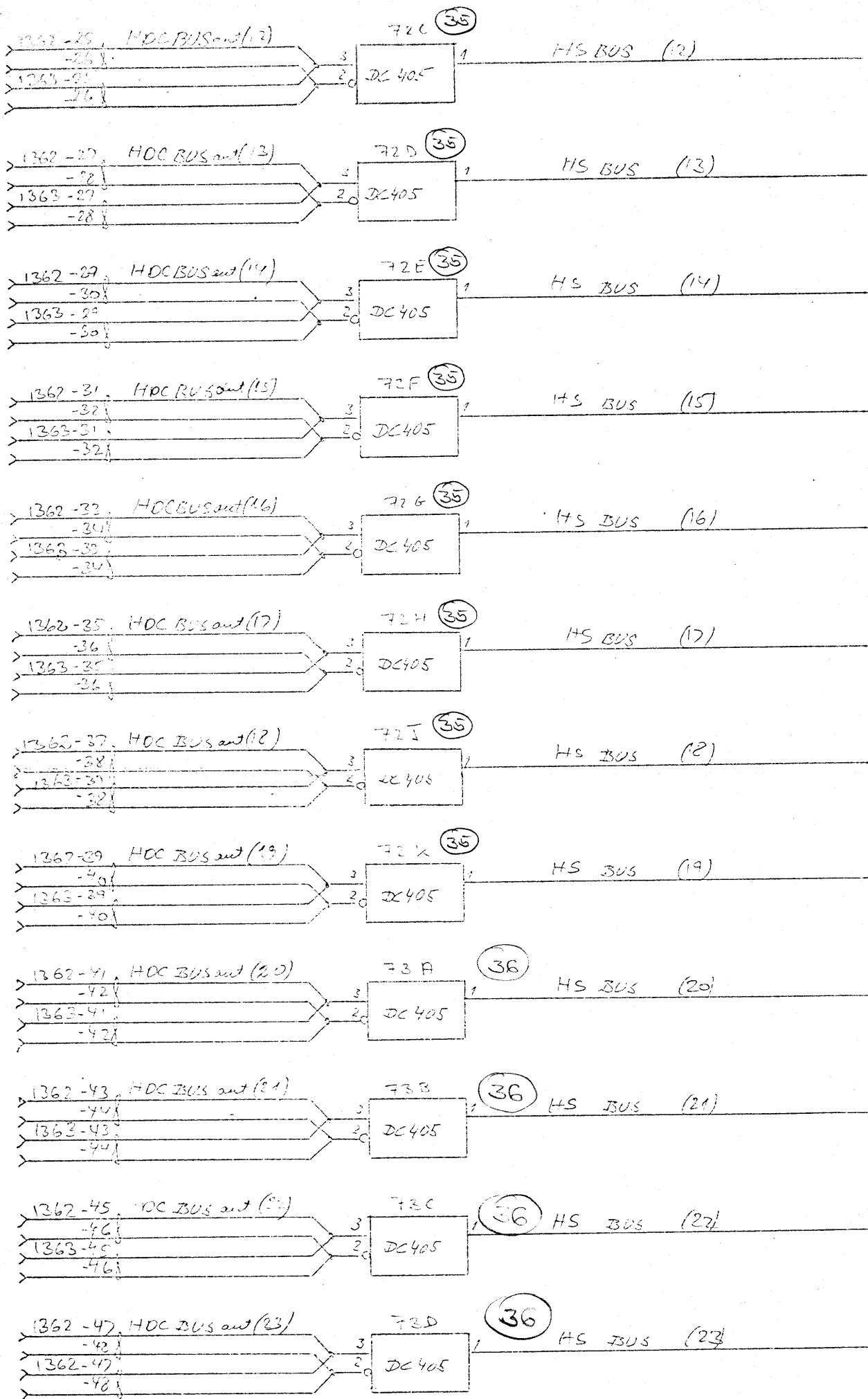
PFC-

PIEP format 2/11/68

A/S REGISTRATION	Designed by	281169 FCK
	Drawn by	
	Dwg. Office	
	Design Check	
	Replaces Orig. No.	
	Due to EON	
	Issued by	



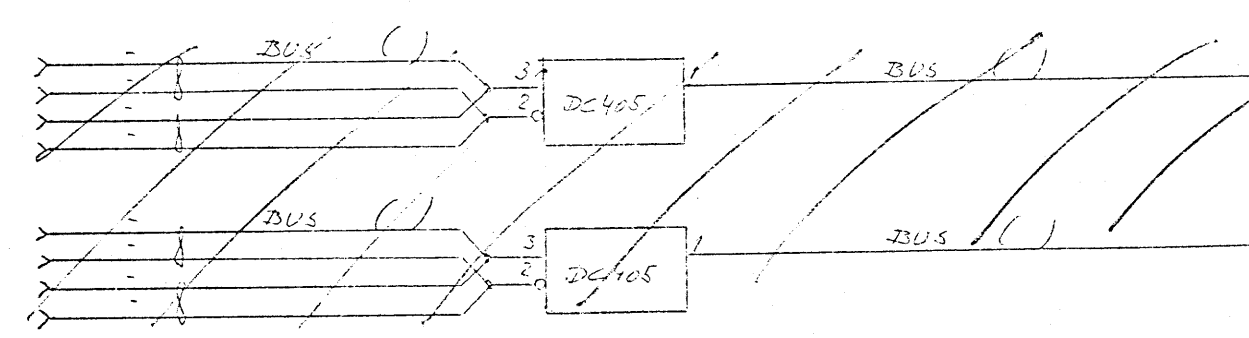
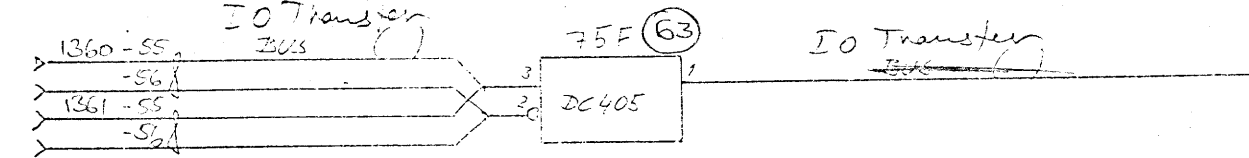
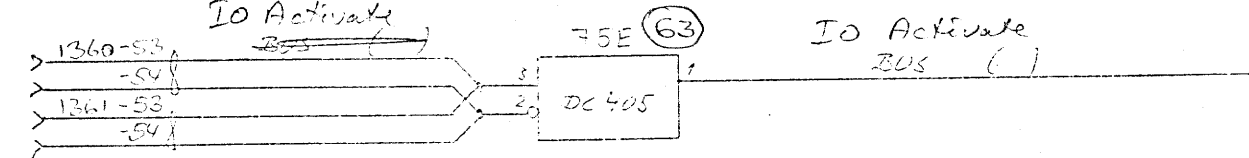
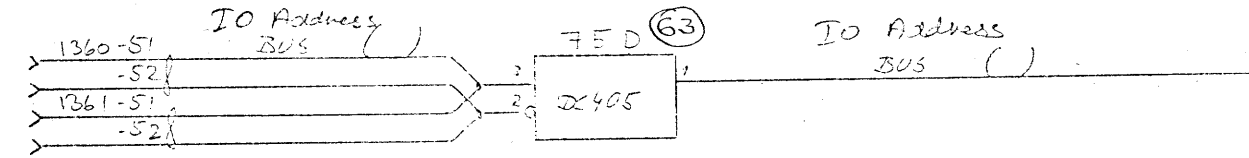
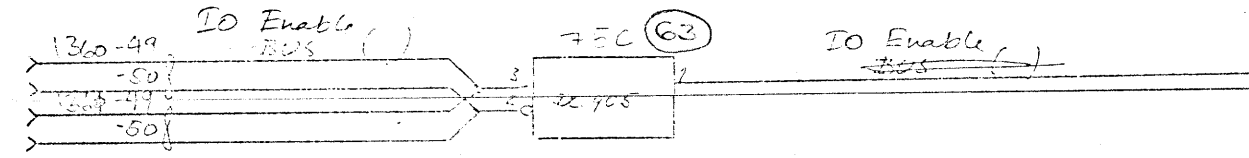
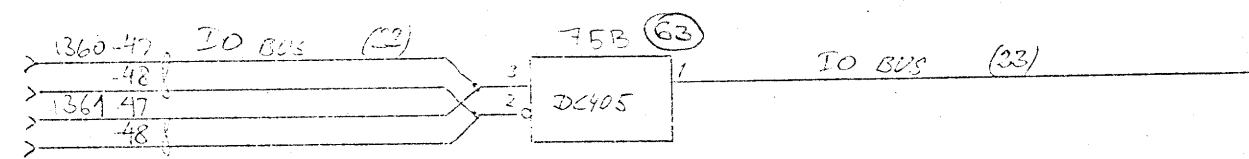
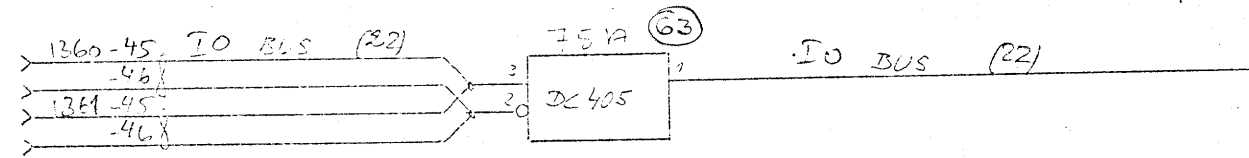
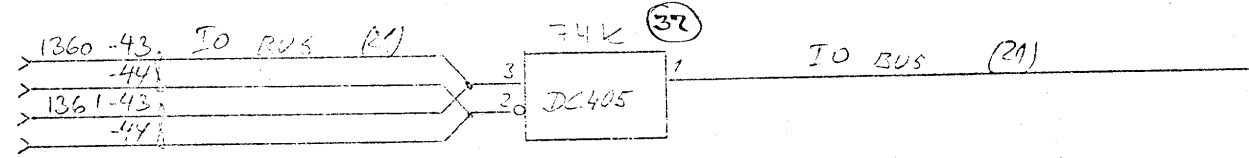
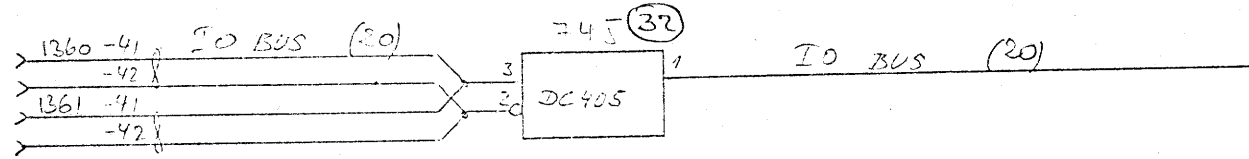
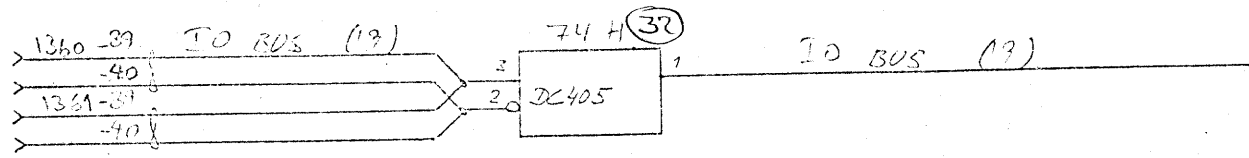
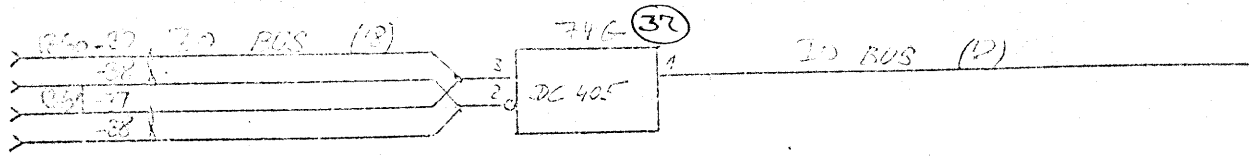
Unit	RC4000	HOC BUS out (0:11)	DPC-632
Dwg. No.	V41832		
Logic Diagram			



REVISION NO. 1  
 DESIGNED BY 281469 VAC  
 DRAWN BY  
 CHECKED BY  
 UNIT RC4000  
 DWG. NO. V11833  
 TITLE HDL BUS out (12:23)  
 LOGIC DIAGRAM

OFC 855





PLEASE FORWARD TO NTRALEN

AS 010 V1 140	Designed by 2814 69	Drawn by	Design Check	Approved by due to LCN	Reviewed by due to LCN
NTRALEN					
Unit RC4000					
Dwg. No. V11835					

Unit RC4000	IO BUS (18-23)	DFC 005
Dwg. No. V11835	IO Enable, IO Address, IO Activate, and IO Transfer Logic Diagram	

